

Compal confidential

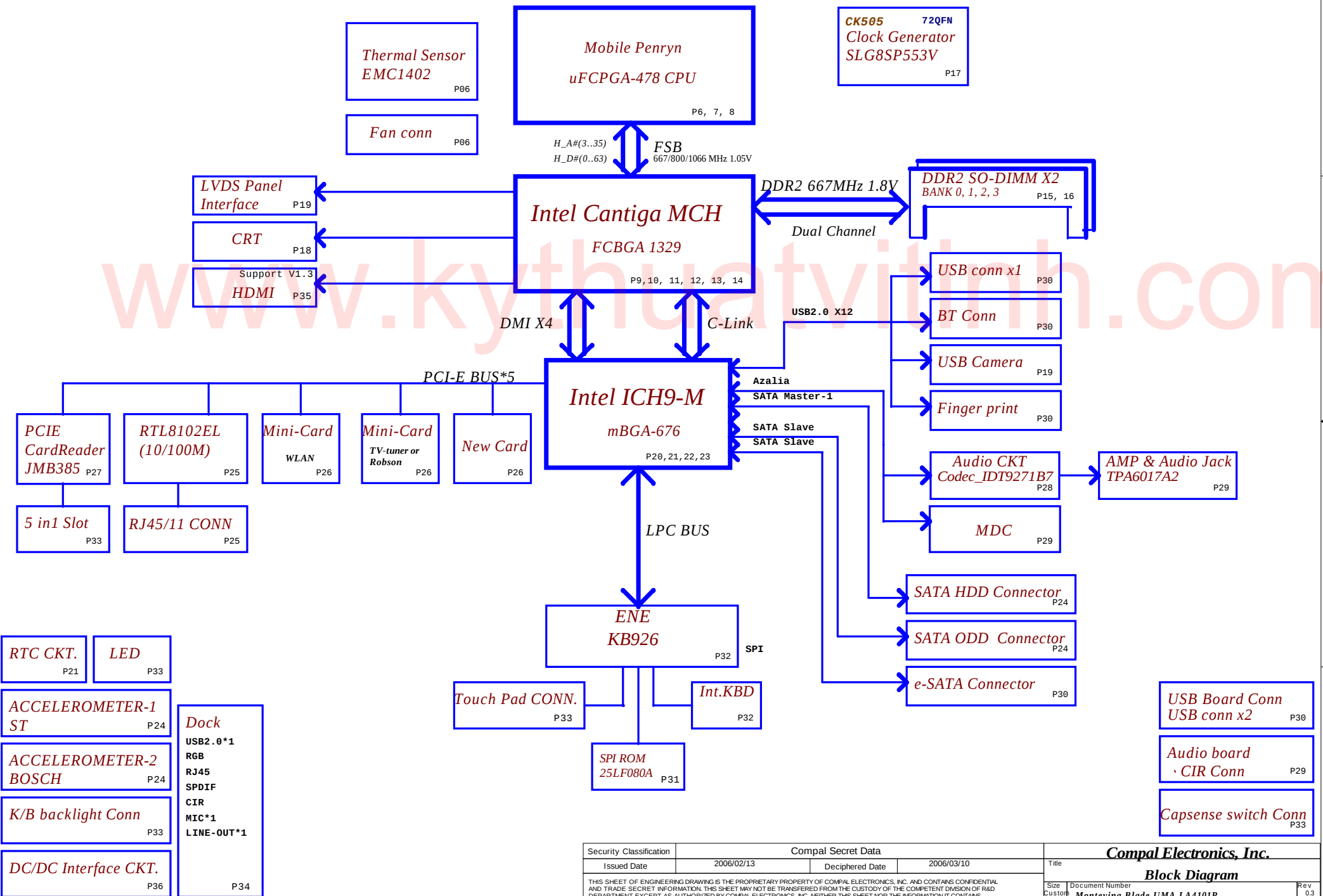
Schematics Document

Mobile Penryn uFCPGA with Intel Cantiga_GM+ICH9-M core logic

2008-01-01

機 密	等級	硬體二部	
	產出人員		
	產出日期		
	解密日期		

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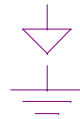
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Voltage Rails

O MEANS ON X MEANS OFF

power plane State	+B	+5VALW +3VALW	+1.8V	+5VS +3VS +1.5VS +0.9V +VCCP +CPU_CORE +2.5VS +1.8VS
S0	O	O	O	O
S1	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

Symbol Note :



: means Digital Ground



: means Analog Ground

@ :	means just reserve , no build
45@ :	means need be mounted when 45 level assy or rework stage.
DEBUG@ :	means just reserve for debug.
BATT @ :	means need be mounted when 45 level assy or rework stage.
CONN@ :	means ME part
ESATA @ :	means just reserve for ESATA
GS @ :	means just reserve for G sensor
FP @ :	means just reserve for Finger Print
Multi @ :	means just reserve for Multi Bay
NewC@ :	means just reserve for New card
DOCK@ :	means just reserve for Docking
Main@ :	means just reserve for Main stream
OPP@ :	means just reserve for OPP
2MiniC@ :	means just reserve for 2nd Mini card slot

USB assignment:

USB-0	Right side
USB-1	Right side
USB-2	Left side(with ESATA)
USB-3	Dock
USB-4	Camera
USB-5	WLAN
USB-6	Bluetooth
USB-7	Finger Printer
USB-8	MiniCard(WWAN/TV)
USB-9	Express card
USB-10	X
USB-11	X

PCIe assignment:

PCIe-1	TV /WWAN/Robeson
PCIe-2	X
PCIe-3	WLAN
PCIe-4	GLAN (Realtek)
PCIe-5	Card reader
PCIe-6	New Card

SMBUS Control Table

	SOURCE	INVERTER	BATT	SERIAL EEPROM	Thermal Sensor	SODIMM	CLK CHIP	MINI CARD	LCD	Cap sensor board	NEW CARD	G sensor
SMB_EC_CK1 SMB_EC_DA1	KB926	X	V	V	X	X	X	X	X	V	X	X
SMB_EC_CK2 SMB_EC_DA2	KB926	X	X	X	V	X	X	X	X	X	X	X
SMB_CK_CLK1 SMB_CK_DAT1	ICH9	X	X	X	X	V	V	V	X	X	V	V
LCD_CLK LCD_DAT	Cantiga	X	X	X	X	X	X	X	V	X	X	X

43154432L01 : Main@/DEBUG@/DOCK@/NewC@/FP@/ESATA@/GS@/Multi@/2MiniC@
 43154432L02 : Main@/DEBUG@/DOCK@/NewC@/FP@/ESATA@/GS@/2MiniC@
 43154432L03 : Main@/DEBUG@/DOCK@/NewC@/FP@/2MiniC@
 43154432L04 : OPP@/DEBUG@
 43154432L05 : OPP@/DEBUG@

I2C / SMBUS ADDRESSING

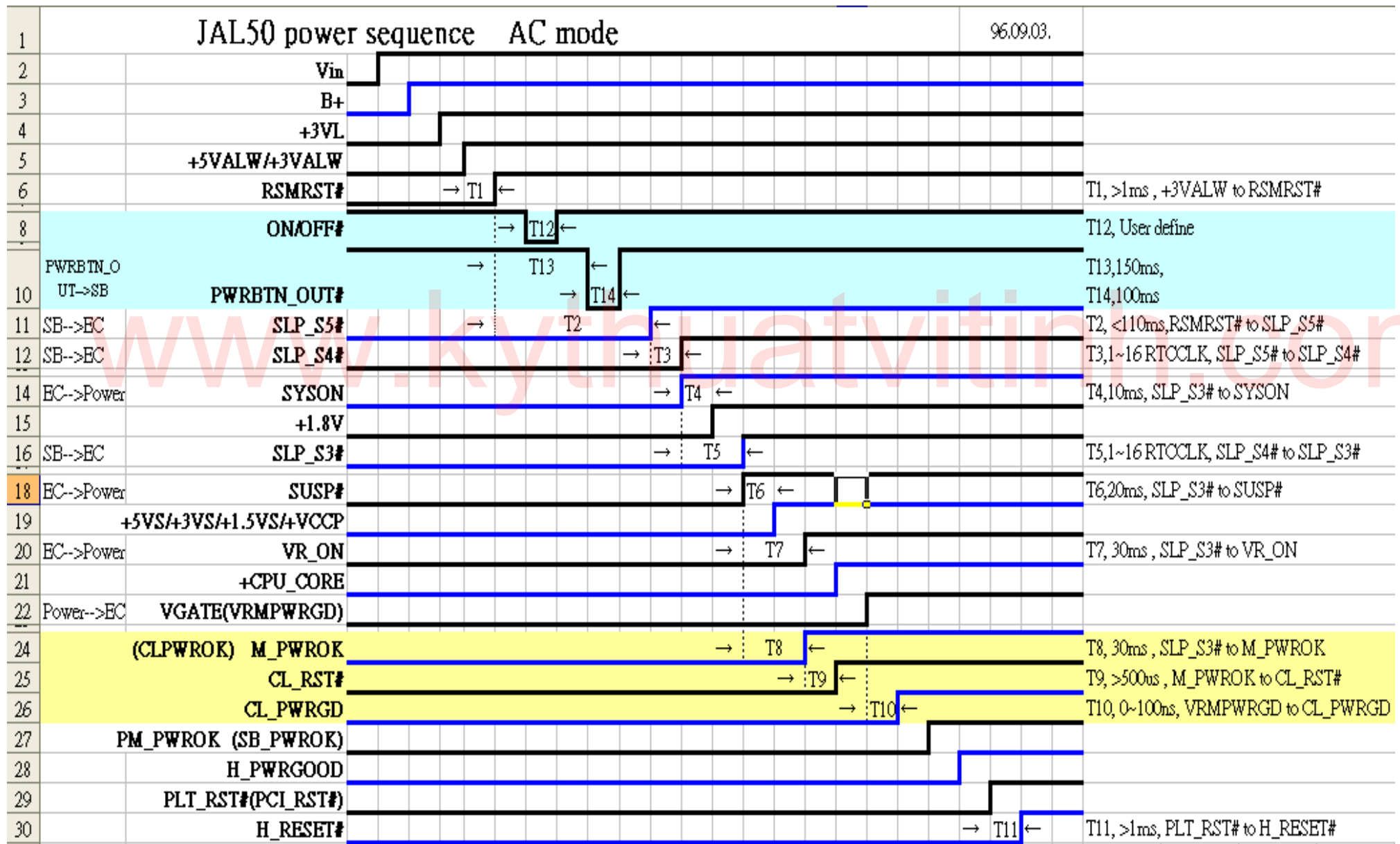
DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

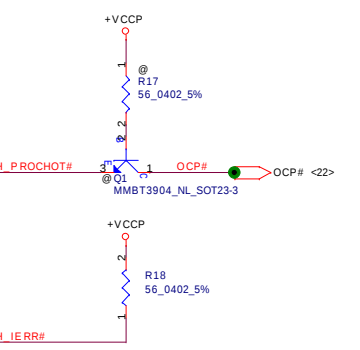
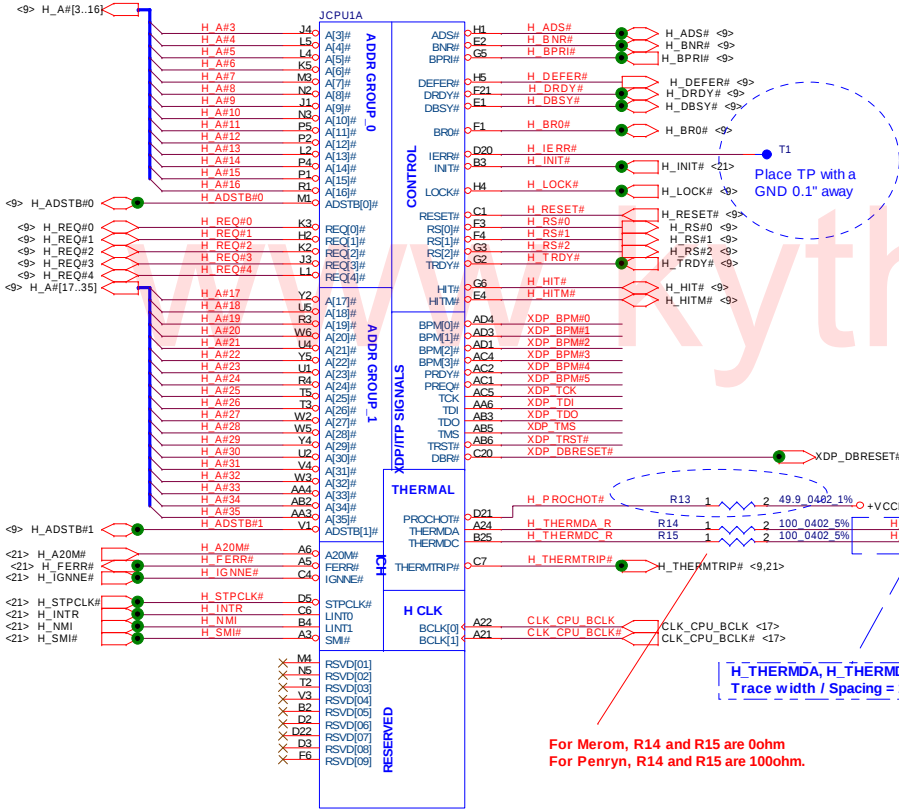
43154432L01 : UMA GM PA FF (SI-1)
 43154432L02 : UMA GM PR FF (SI-1)
 43154432L03 : UMA GL PR FF-
 43154432L04 : UMA GM OPP (SI-1)
 43154432L05 : UMA GL OPP

Cantiga GM45 B0(QR32) : SA00001P930
 ICH9M A2 ES2 Base : SA00002AN10

DA600007100 --->Main
 DAZ03V00100 --->OPP

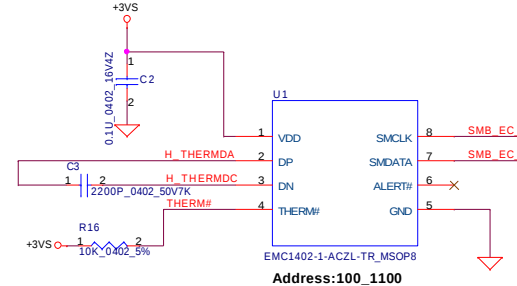
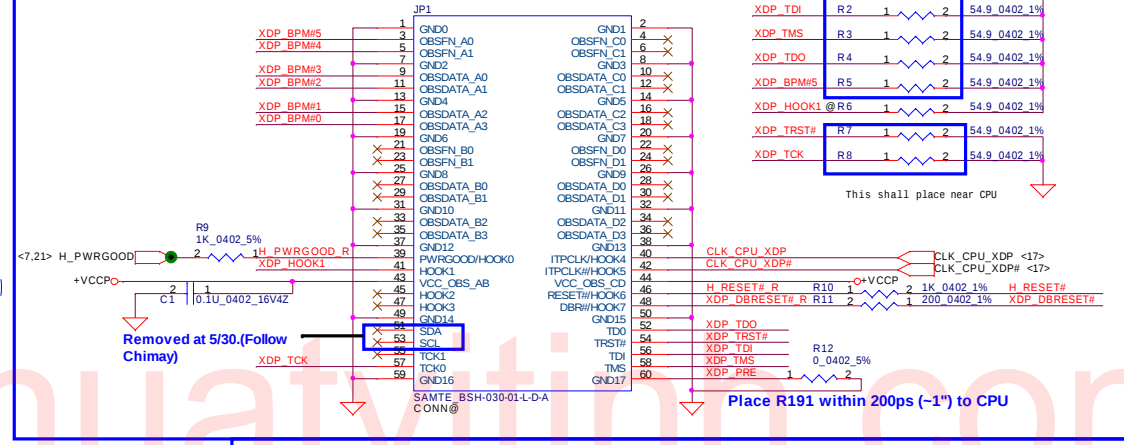
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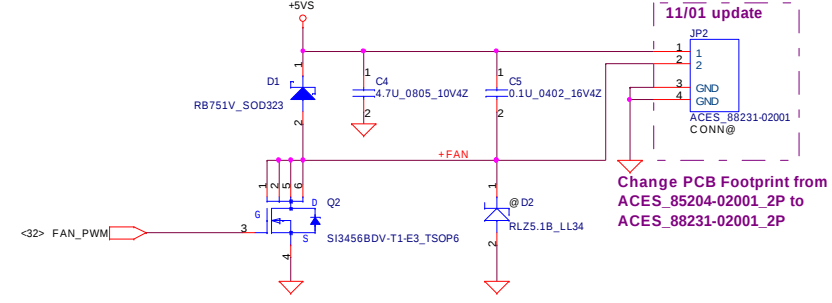


For Merom, R14 and R15 are 0ohm
For Penryn, R14 and R15 are 100ohm.

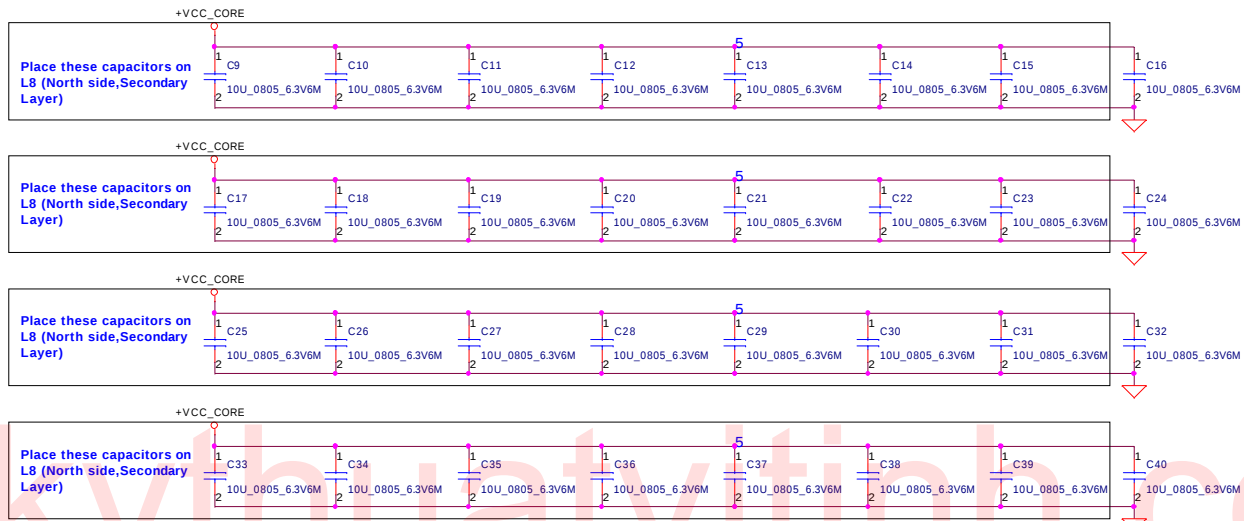
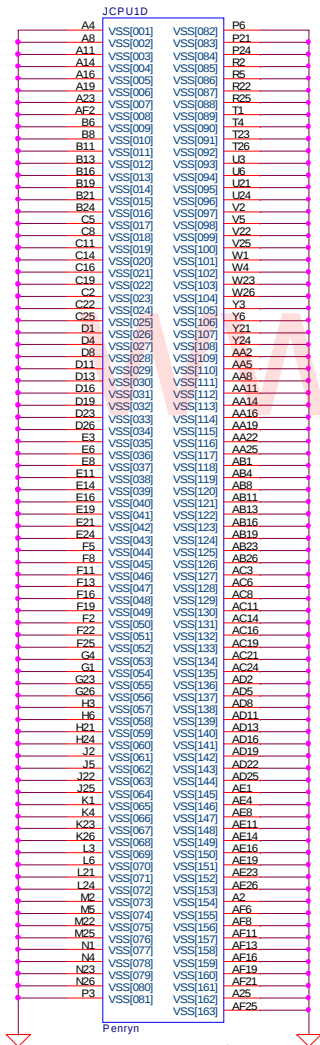
ITP-XDP Connector



PWM Fan Control circuit



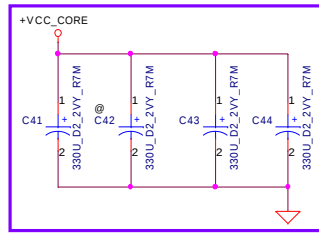
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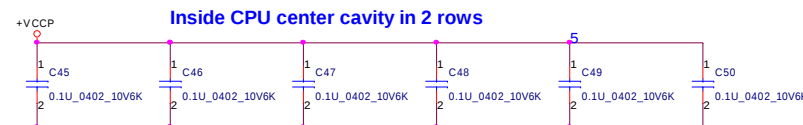
Mid Frequency Decoupling

Near CPU CORE regulator

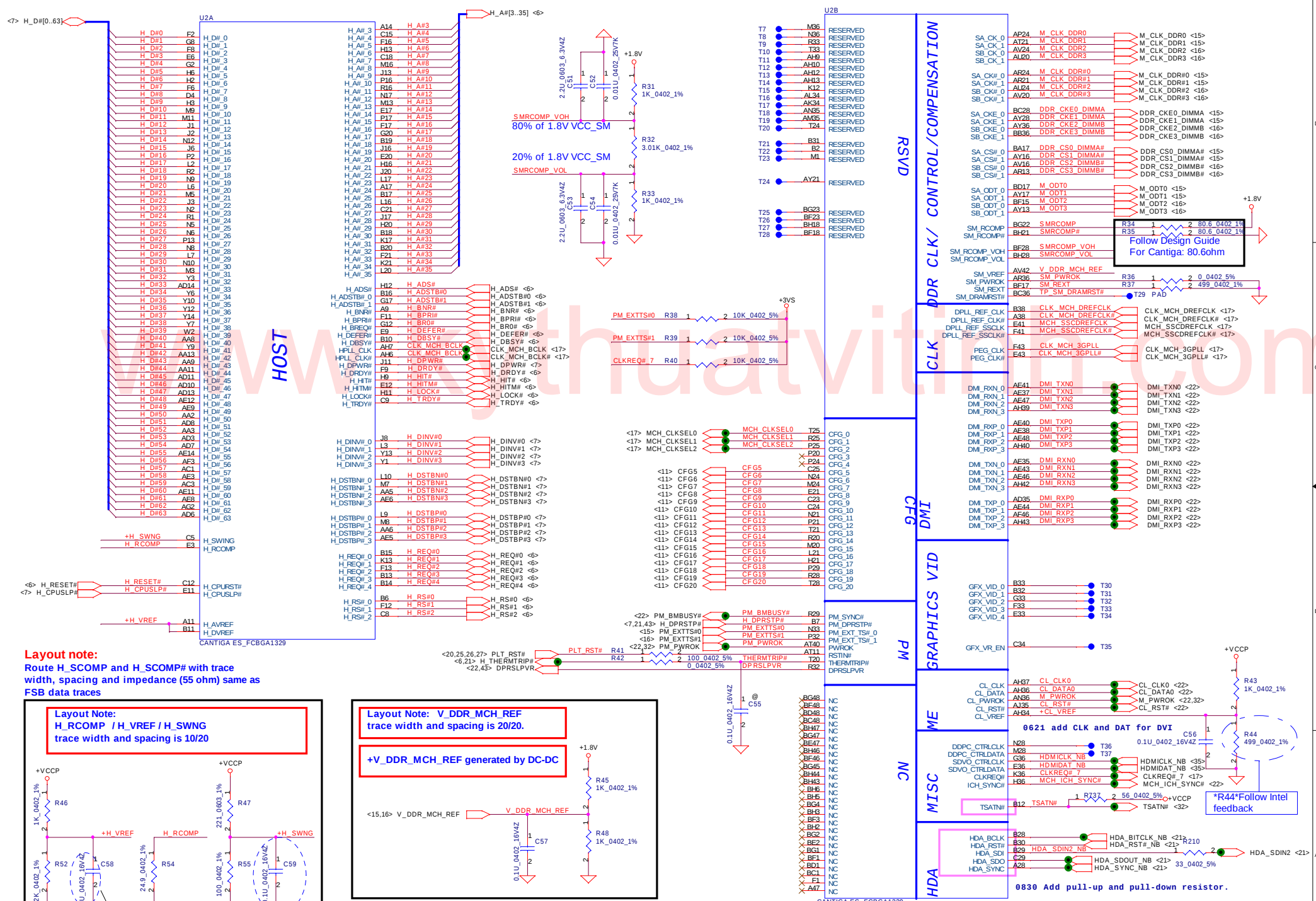
ESR <= 1.5m ohm
Capacitor > 1980uF



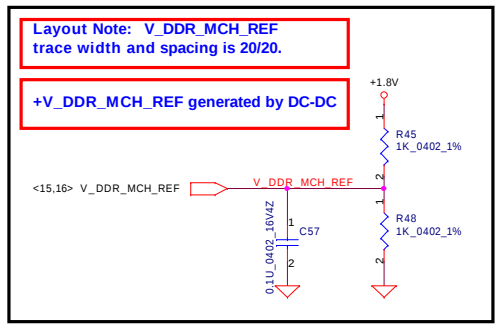
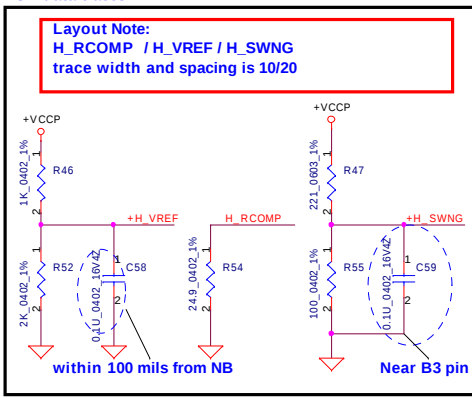
11/21 Change ESR=7m ohm



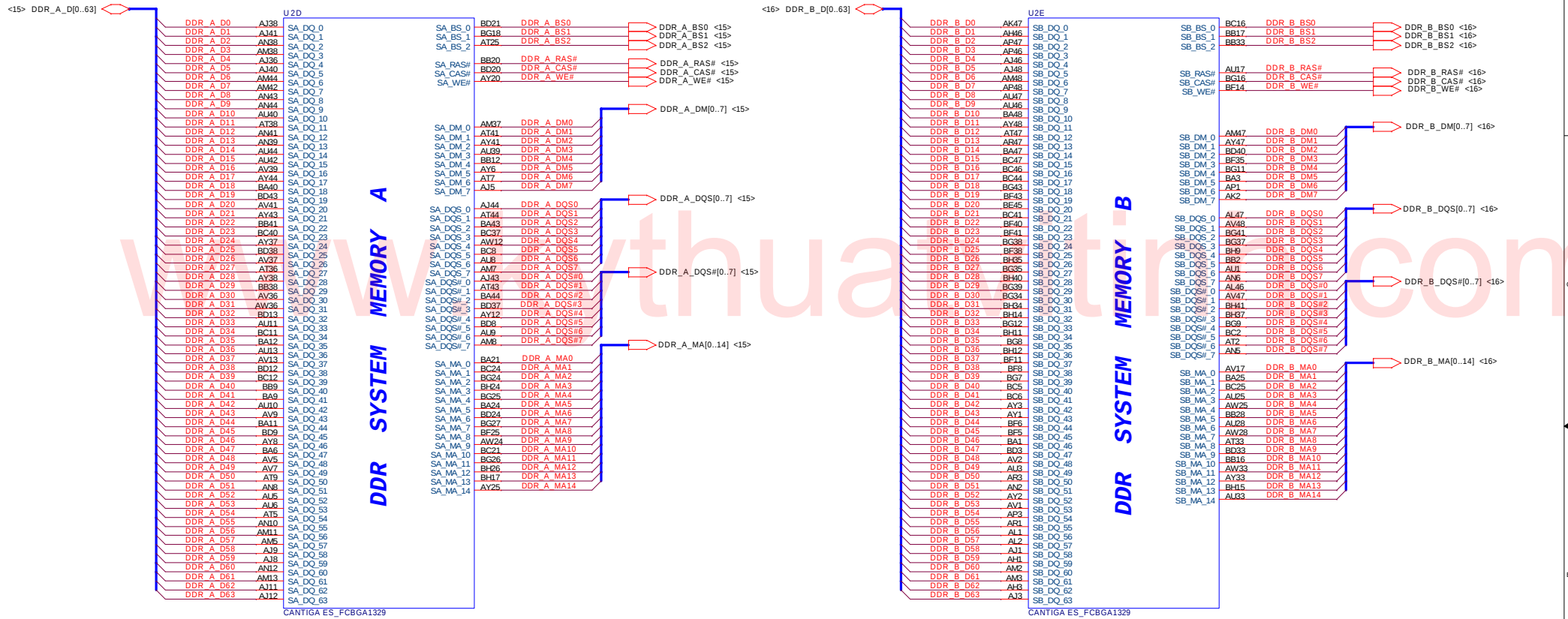
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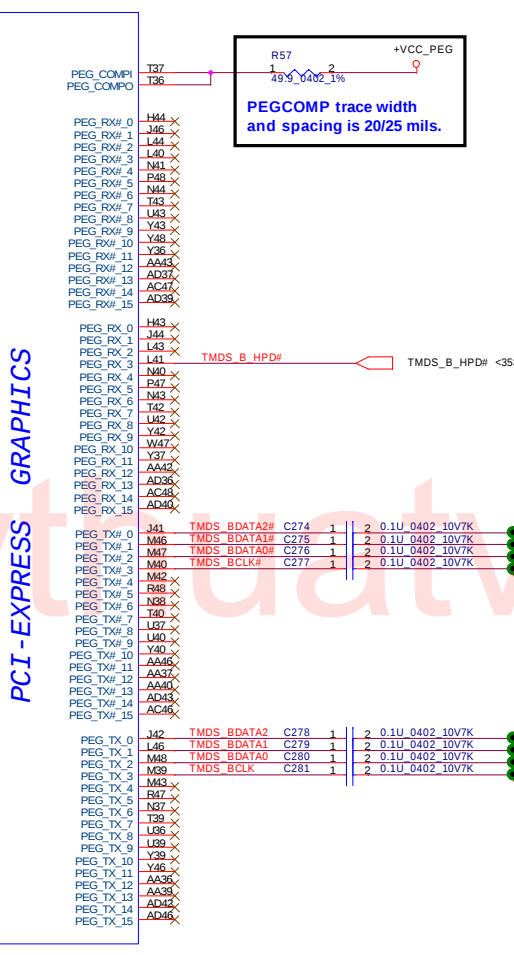
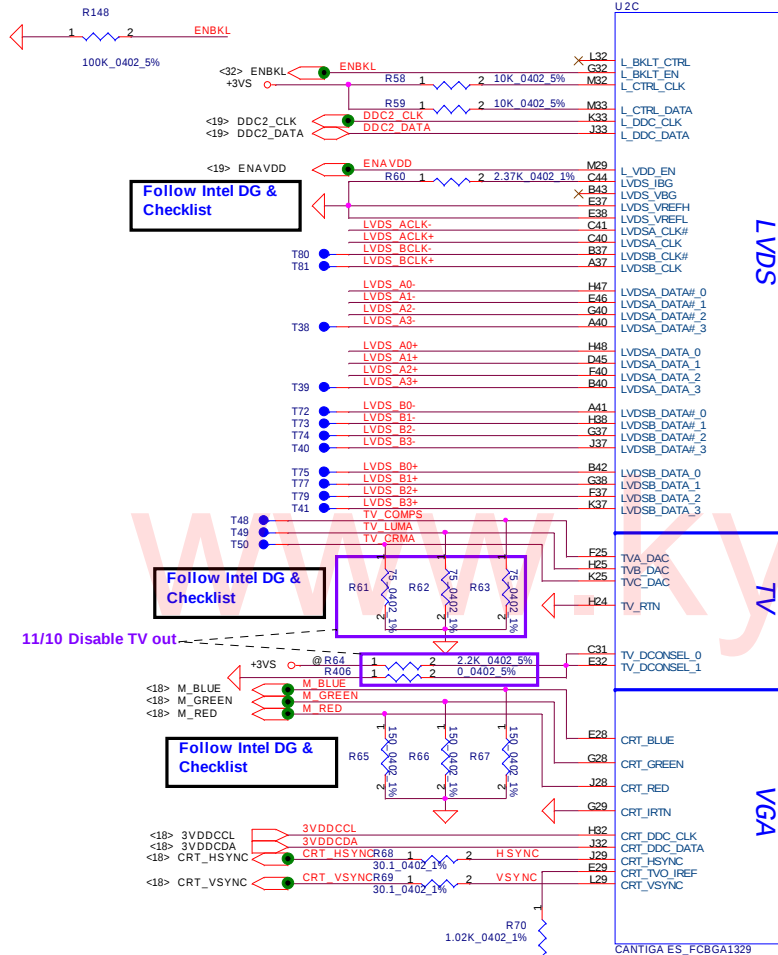
Layout note:
Route H_SCOMP and H_SCOMP# with trace width, spacing and impedance (55 ohm) same as FSB data traces



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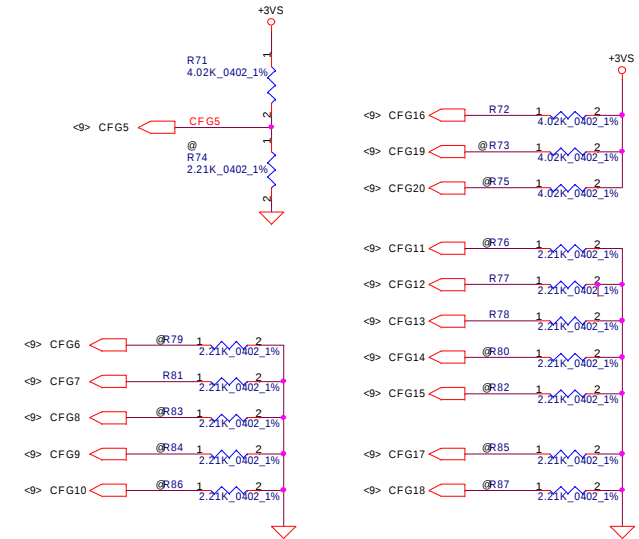
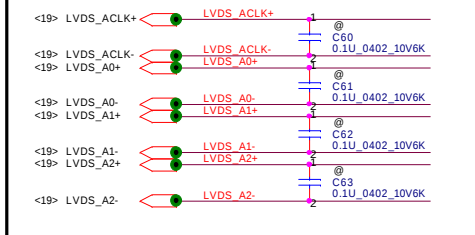
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Strap Pin Table

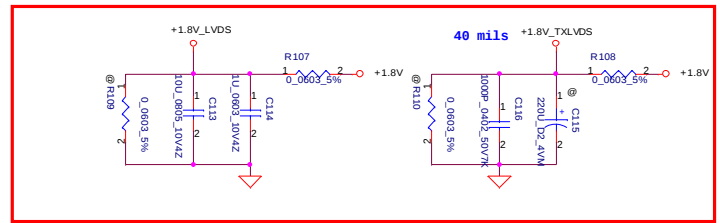
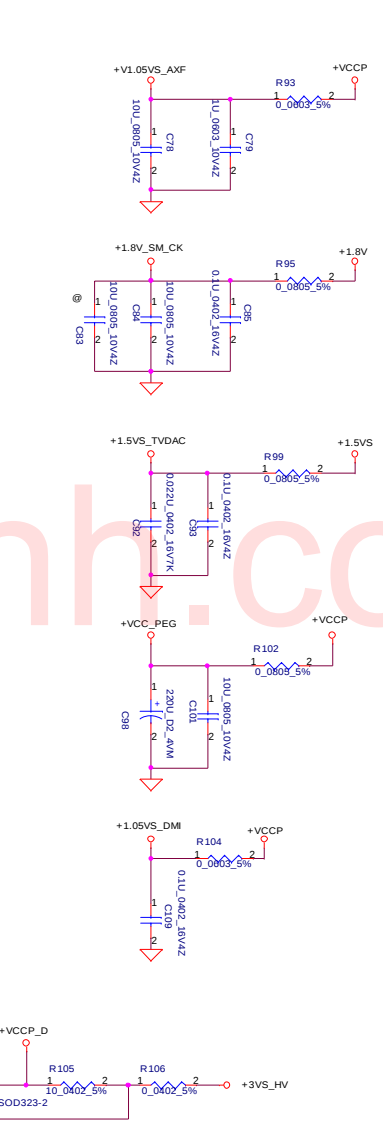
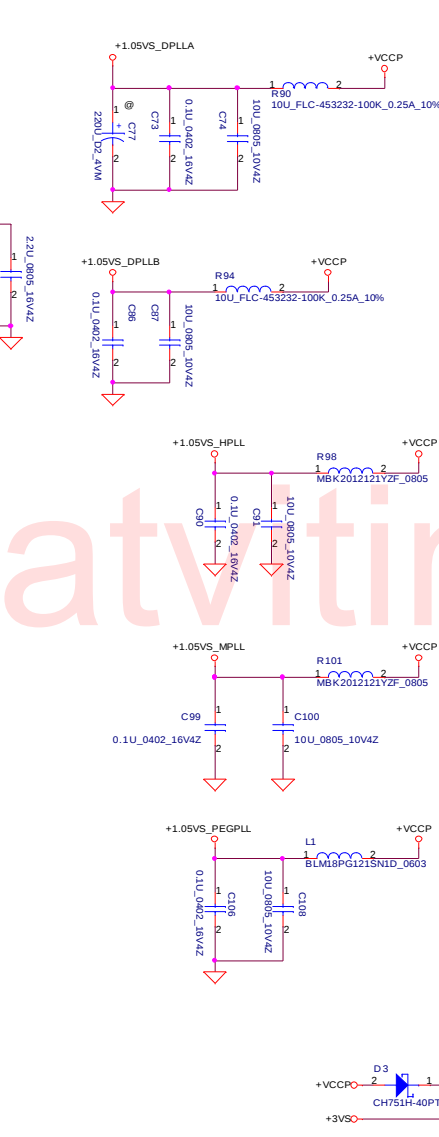
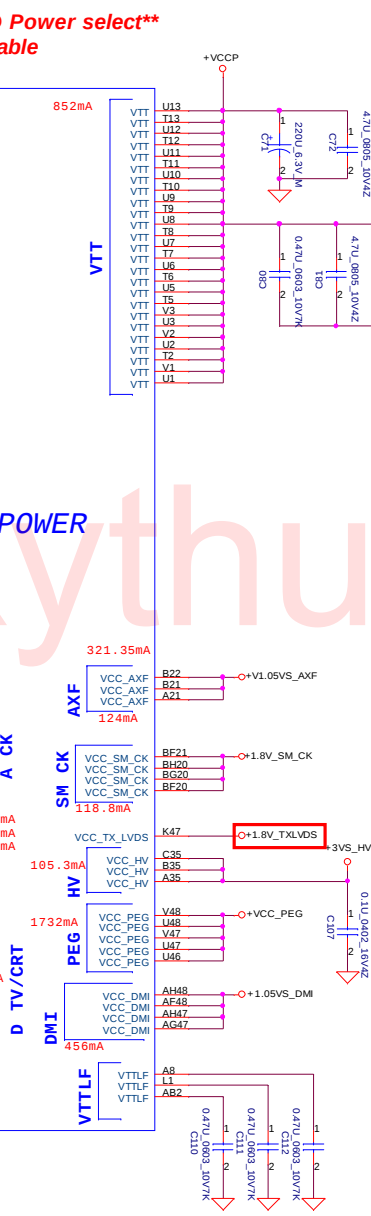
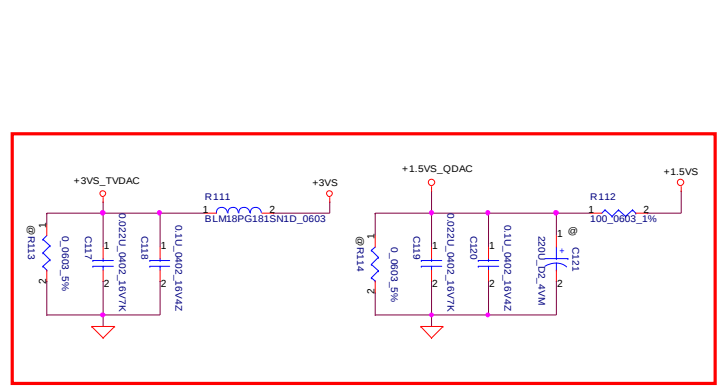
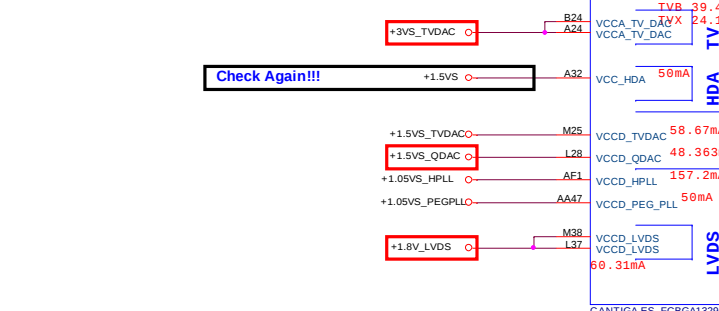
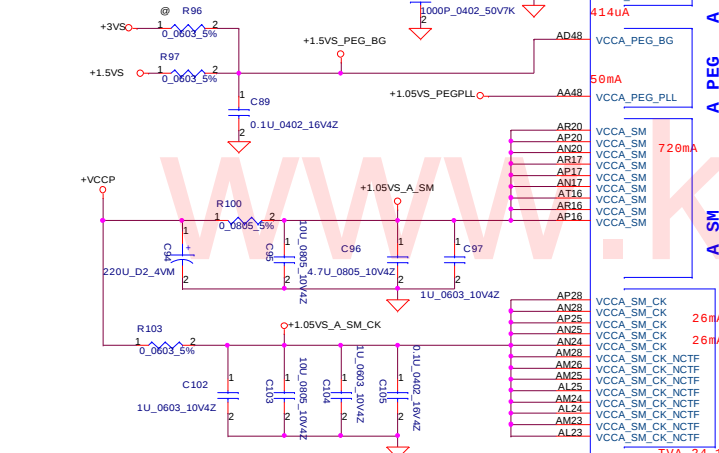
CFG[2:0] FSB Freq select	000 = FSB 1066MHz 010 = FSB 800MHz 011 = FSB 667MHz Others = Reserved
CFG[4:3]	Reserved
CFG5 (DMI select)	0 = DMI x 2 1 = DMI x 4 *
CFG6	0 = The iTPM Host Interface is enable 1 = The iTPM Host Interface is disable *
CFG7 (Intel Management Engine Crypto strap)	0 =(TLS)chiper suite with no confidentiality 1 =(TLS)chiper suite with confidentiality *
CFG8	Reserved
CFG9 (PCIe Graphics Lane Reversal)	0 = Reverse Lane,15>0, 14>1 1 = Normal Operation,Lane Number in order *
CFG10 (PCIe Lookback enable)	0 = Enable 1 = Disable *
CFG11	Reserved
CFG[13:12] (XOR/ALLZ)	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation (Default) *
CFG[15:14]	Reserved
CFG16 (FSB Dynamic ODT)	0 = Disabled 1 = Enabled *
CFG[18:17]	Reserved
CFG19 (DMI Lane Reversal)	0 = Normal Operation (Lane number in Order) * 1 = Reverse Lane
CFG20 (PCIe/SDVO concurrent)	0 = Only PCIe or SDVO is operational. * 1 = PCIe/SDVO are operating simu.

Solve 3G WWAN issue



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****RED Mark: Means UMA & dis@ Power select****
~It check by INTEL Graphics Disable Guidelines~

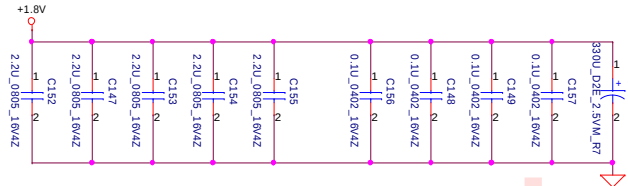




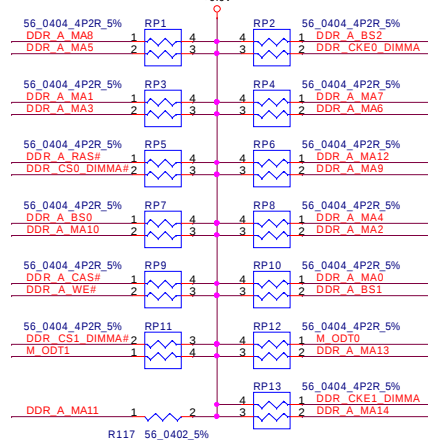
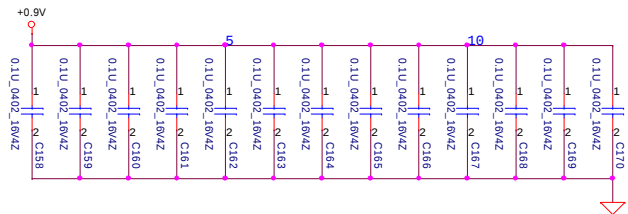
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<10> DDR_A_DQS#[0..7]
 <10> DDR_A_D[0..63]
 <10> DDR_A_DM[0..7]
 <10> DDR_A_DQS[0..7]
 <10> DDR_A_MA[0..14]

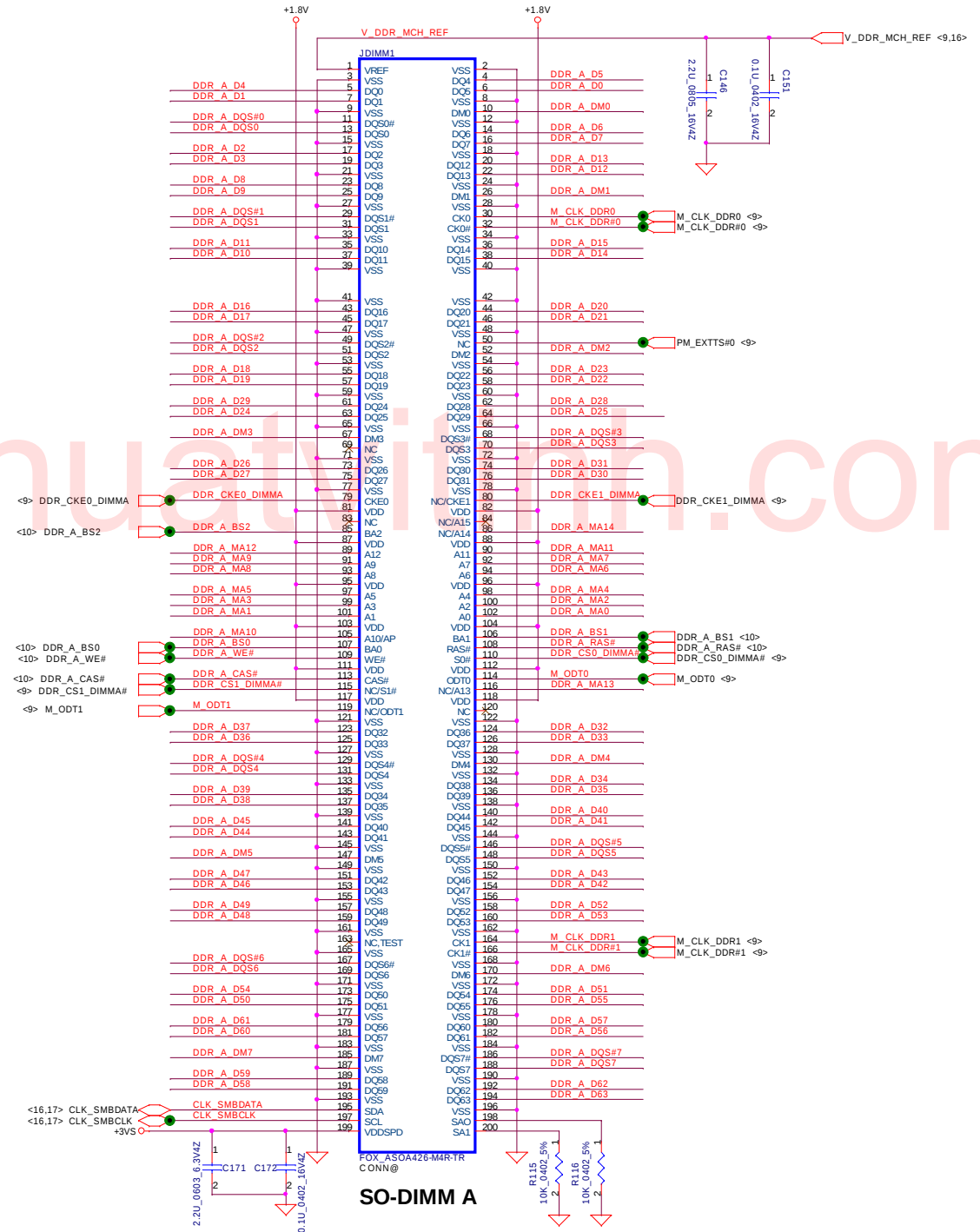
Layout Note:
Place near JP3



Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V

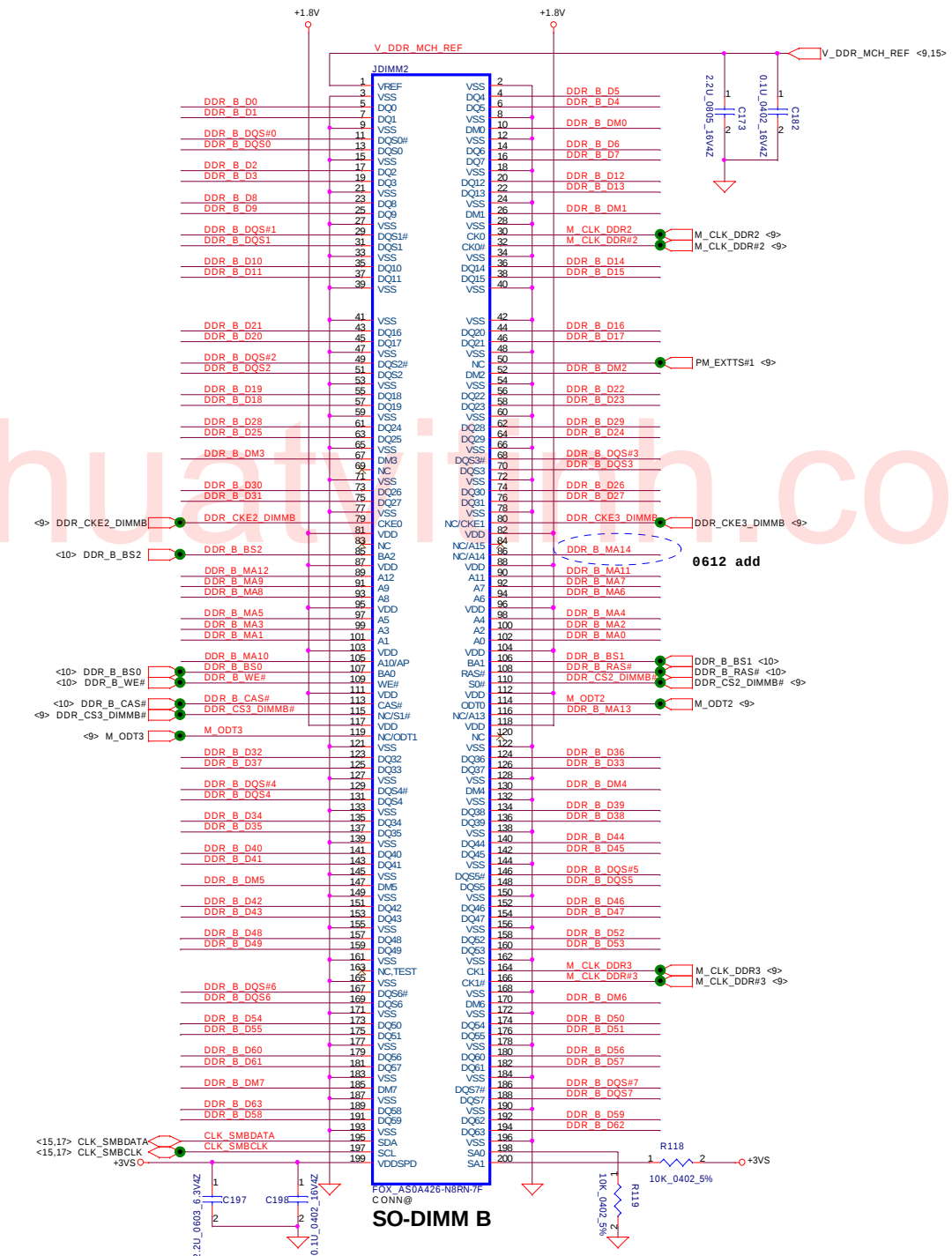
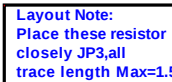
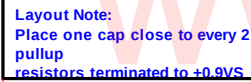
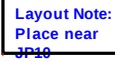


Layout Note:
Place these resistor closely JP3, all trace length Max=1.5"



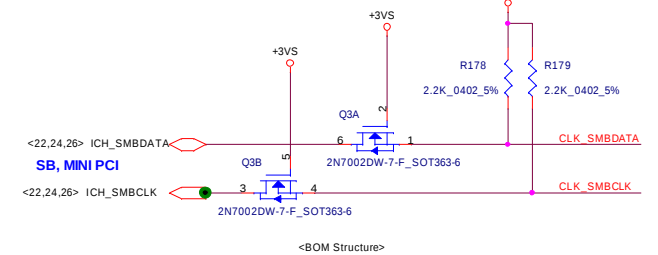
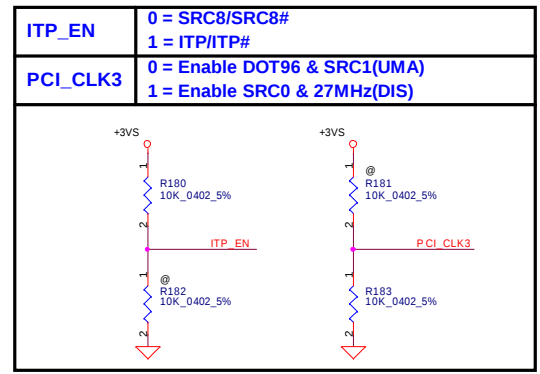
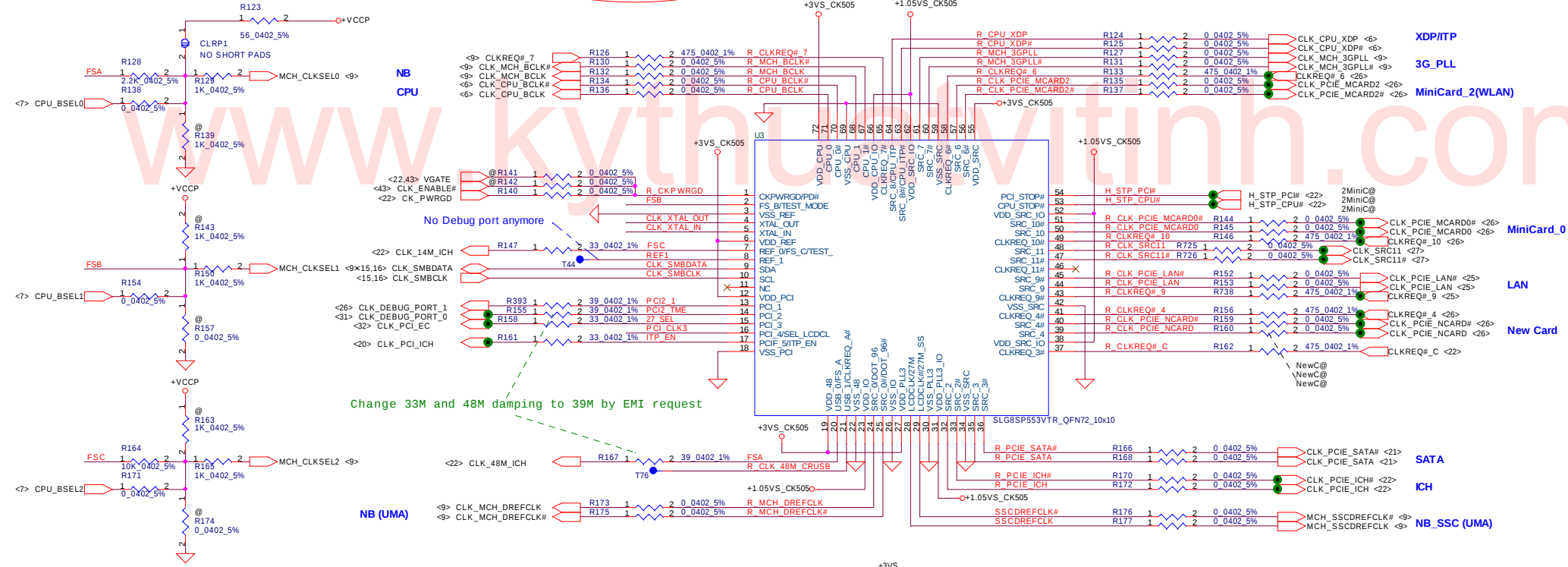
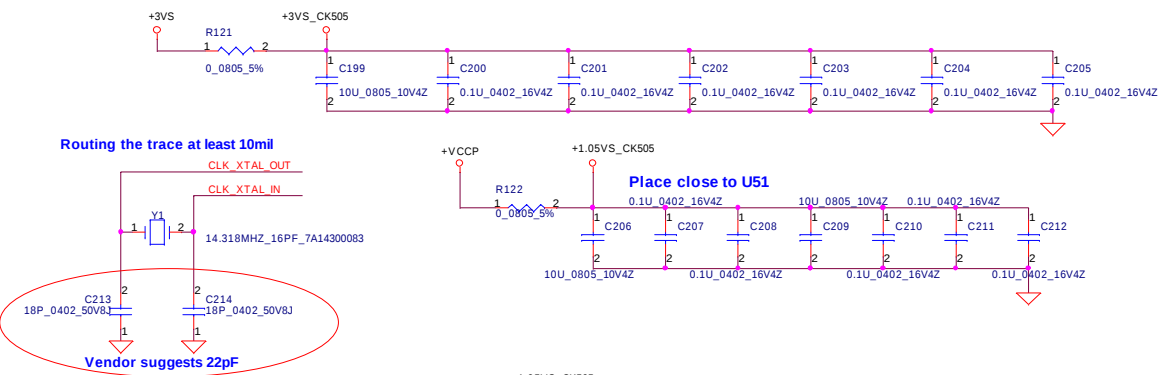
SO-DIMM A

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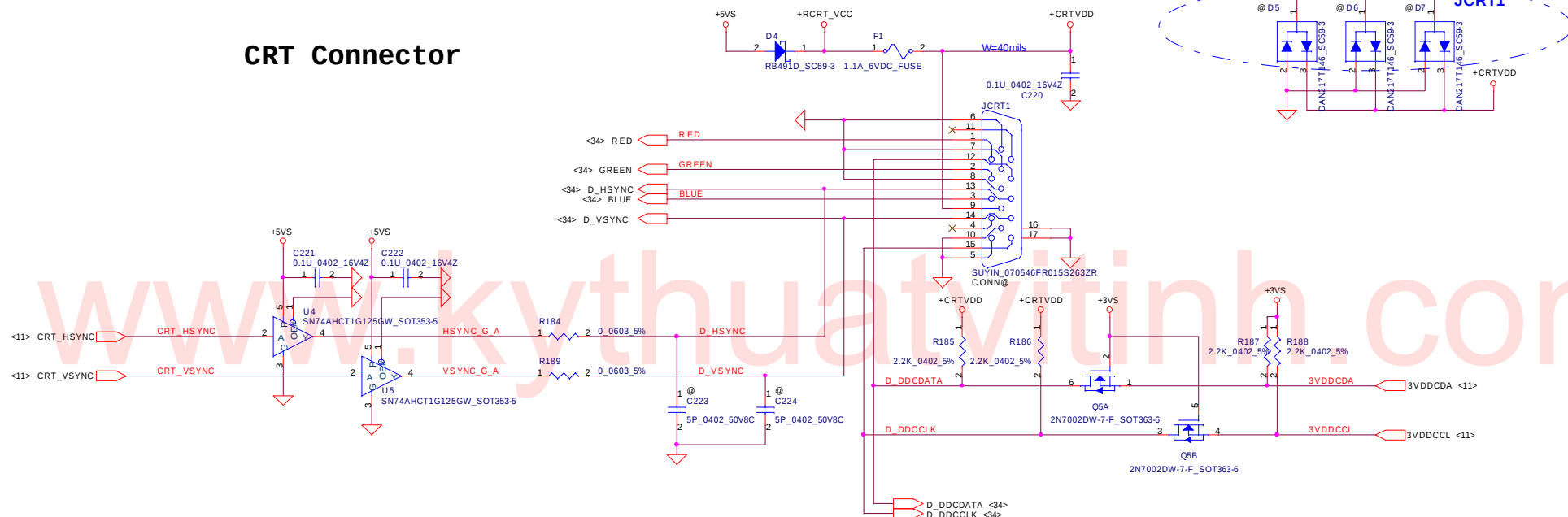
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FSC	FSB	FSA	CPU	SRC	PCI	REF	DOT_96	USB
CLKSEL2	CLKSEL1	CLKSEL0	MHz	MHz	MHz	MHz	MHz	MHz
0	0	0	266	100	33.3	14.318	96.0	48.0
0	0	1	133	100	33.3	14.318	96.0	48.0
0	1	0	200	100	33.3	14.318	96.0	48.0
0	1	1	166	100	33.3	14.318	96.0	48.0
1	0	0	333	100	33.3	14.318	96.0	48.0
1	0	1	100	100	33.3	14.318	96.0	48.0
1	1	0	400	100	33.3	14.318	96.0	48.0
1	1	1						
Reserved								



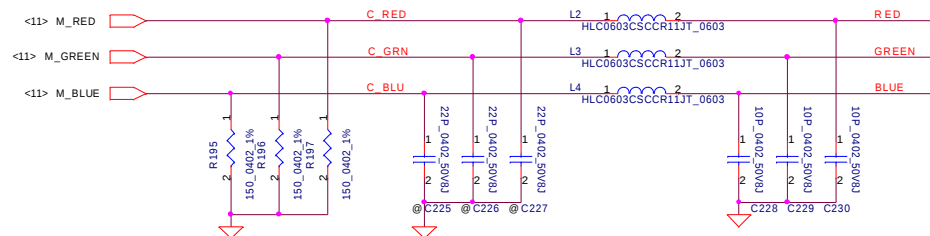
@C215	5P_0402_50V8C	2	1	CLK_48M_ICH
@C216	4.7P_0402_50V8C	2	1	CLK_14M_ICH
@C217	4.7P_0402_50V8C	2	1	CLK_PCI_ICH
@C218	4.7P_0402_50V8C	2	1	CLK_PCI_EC
@C219	5P_0402_50V8C	2	1	CLK_DEBUG_PORT_0

CRT Connector



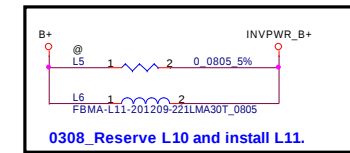
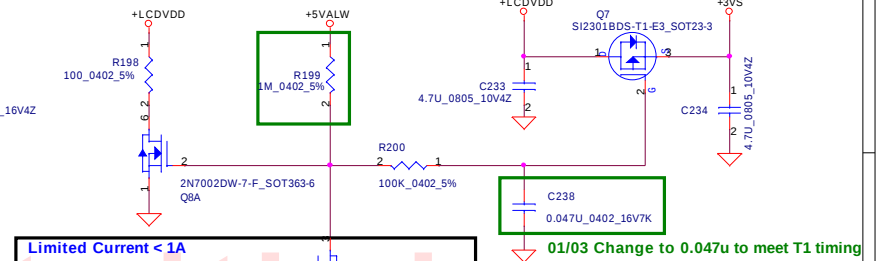
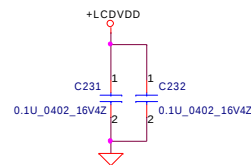
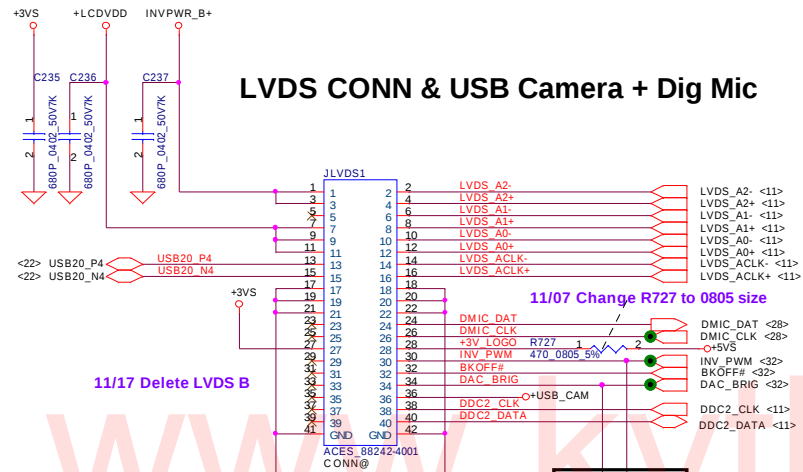
CRT Termination/EMI Filter

11/07 Change CRT louting NB-->Docking-->CRT connector

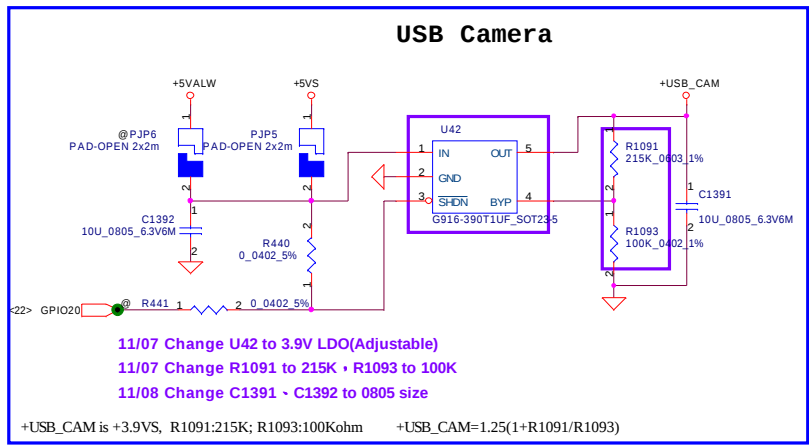


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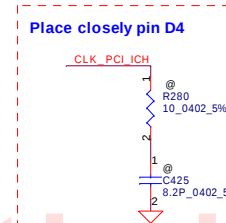
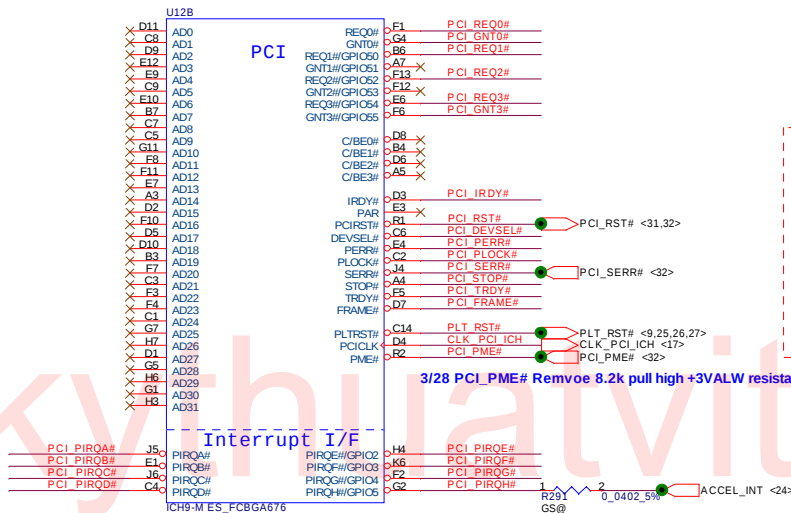
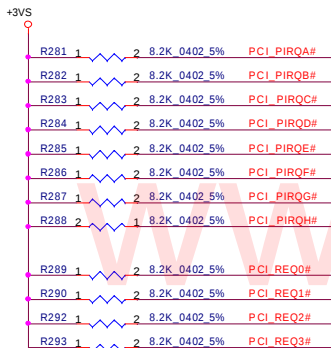
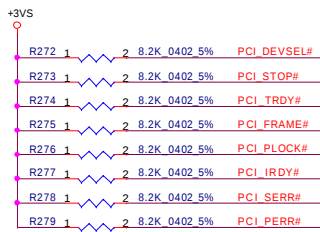
LVDS CONN & USB Camera + Dig Mic



USB Camera

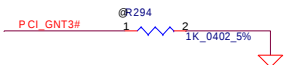


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				Date:	Rev
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				Sheet	19 of 46



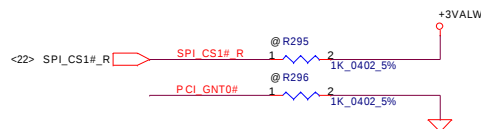
A16 swap override Strap

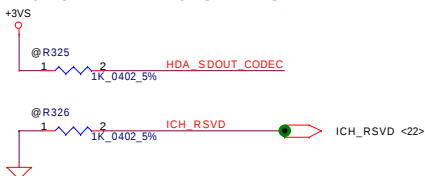
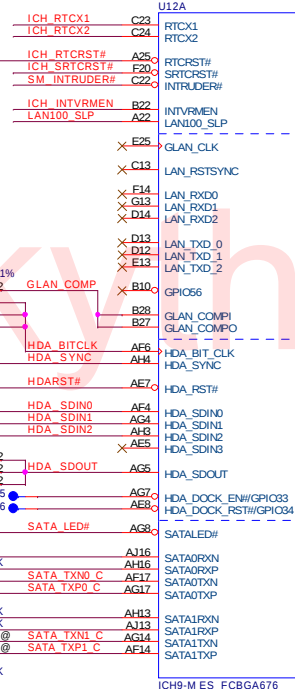
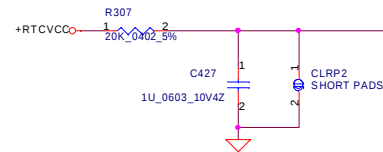
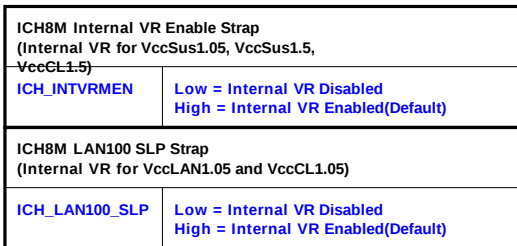
PCI_GNT3# Low= A16 swap override Enble
High= Default *



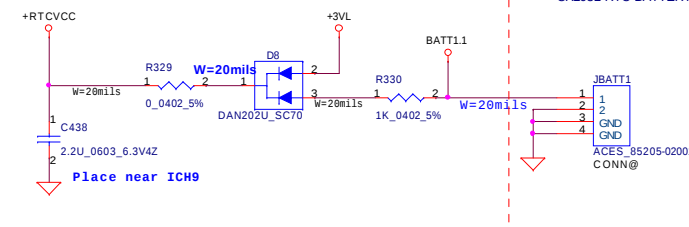
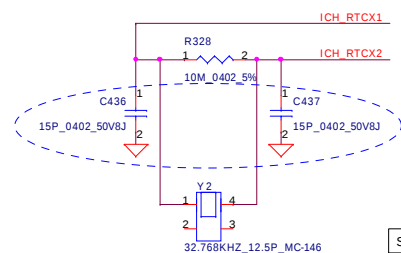
Boot BIOS Strap

PCI_GNT0#	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC *

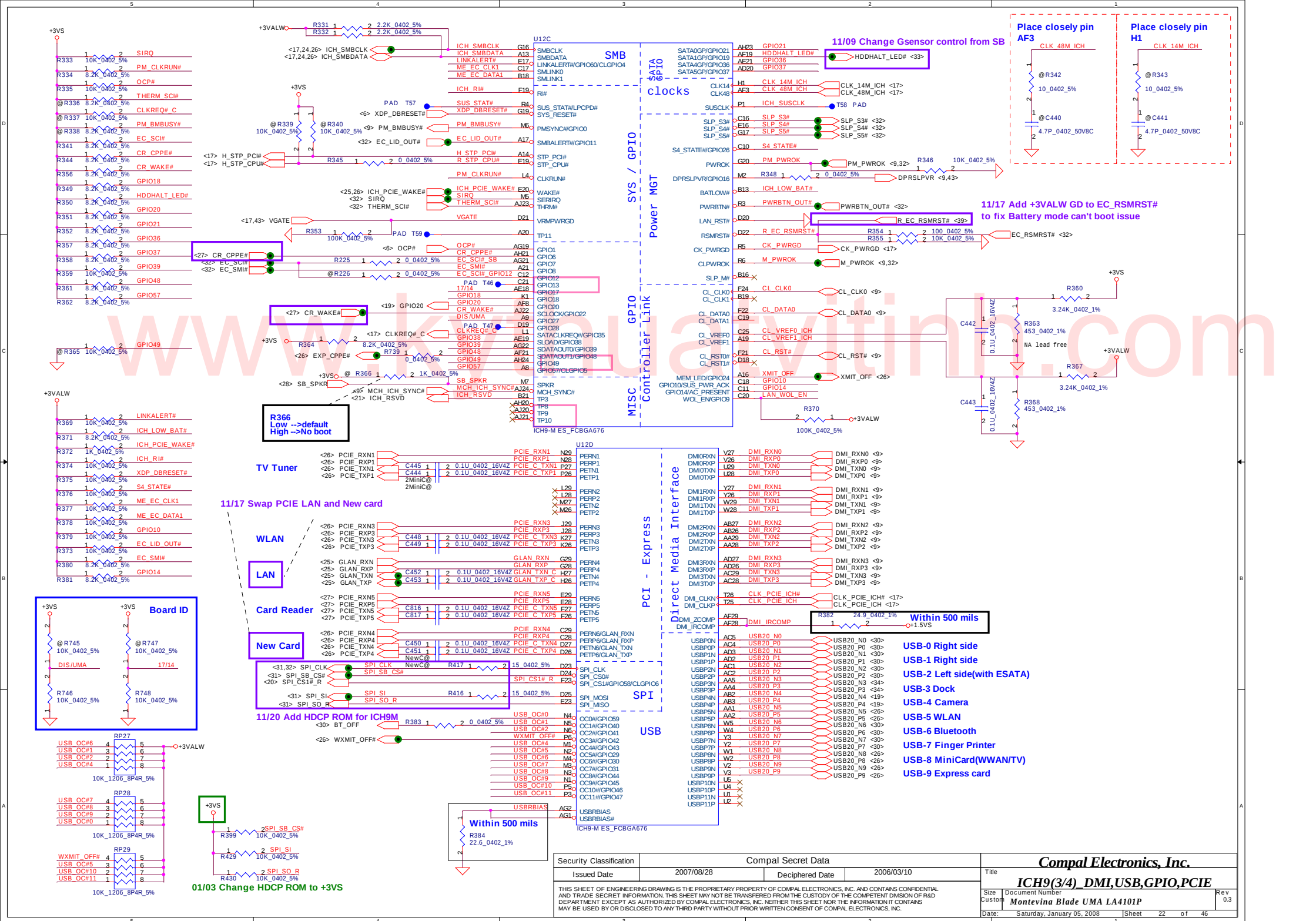


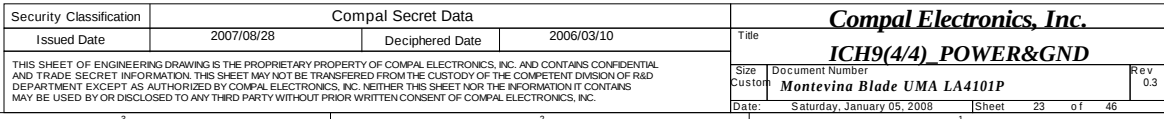


ICH_RSVD	HDA_SDOUT_CODEC	
0	0	
0	1	
1	0	
1	1	

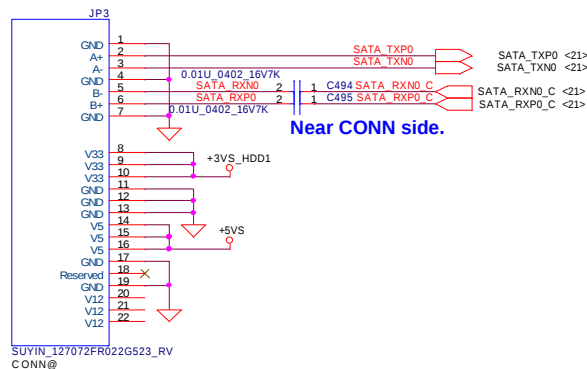


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					Size	Document Number				Rev
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					Date:	Saturday, January 05, 2008	Sheet	21	of	46

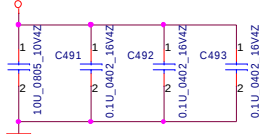




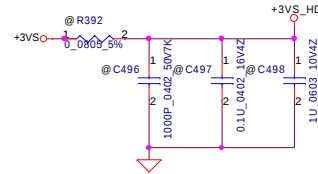
HDD Connector



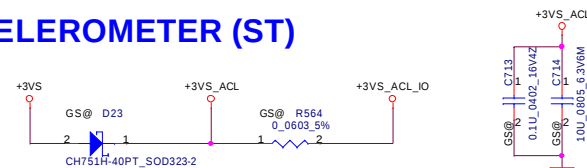
Place near HDD CONN (JP3)



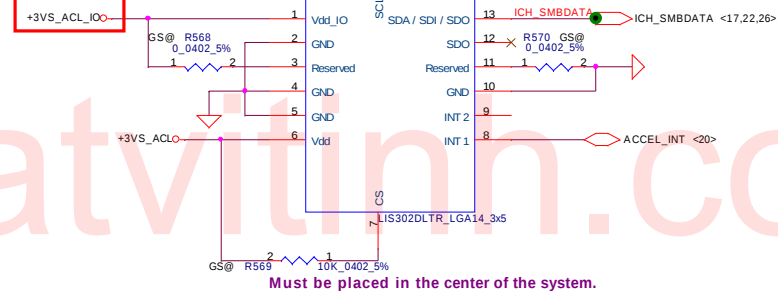
Place near HDD CONN



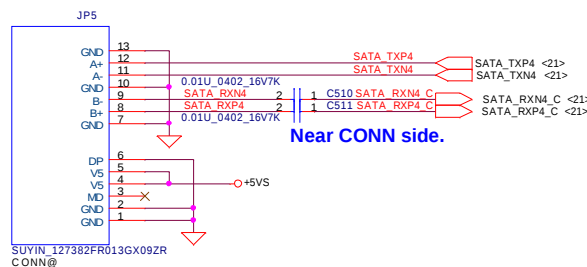
ACCELEROMETER (ST)



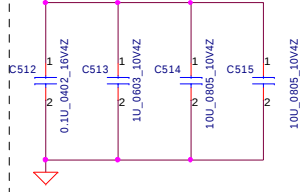
VDDIO absolute man
rating is VDD+0.1



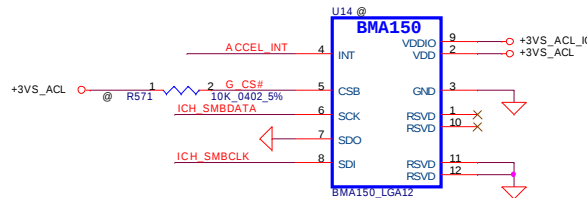
CD-ROM Connector



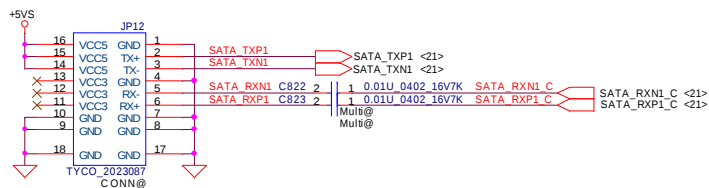
Place caps. near ODD CONN.



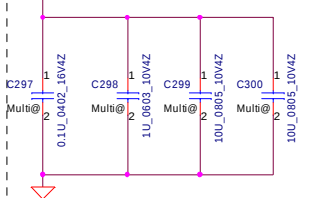
ACCELEROMETER (Bosch)



Multi Bay

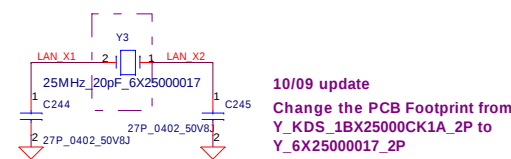
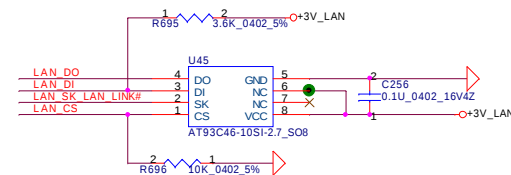
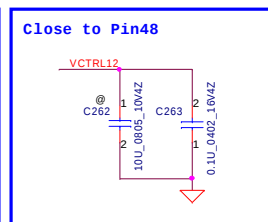
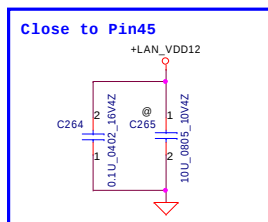
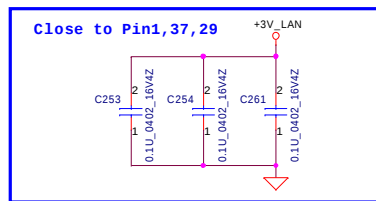
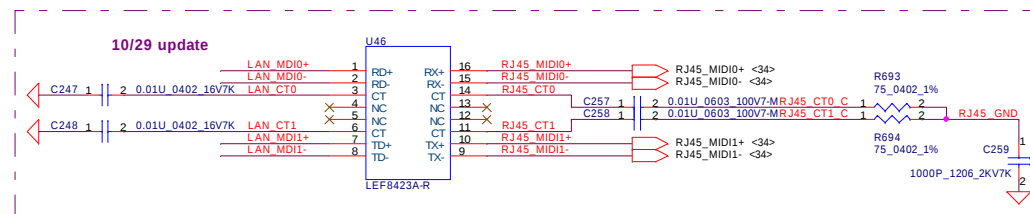
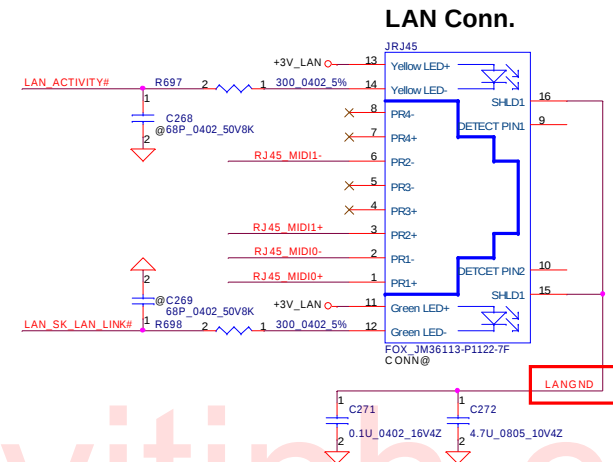


Place caps. near Multi Bay CONN.



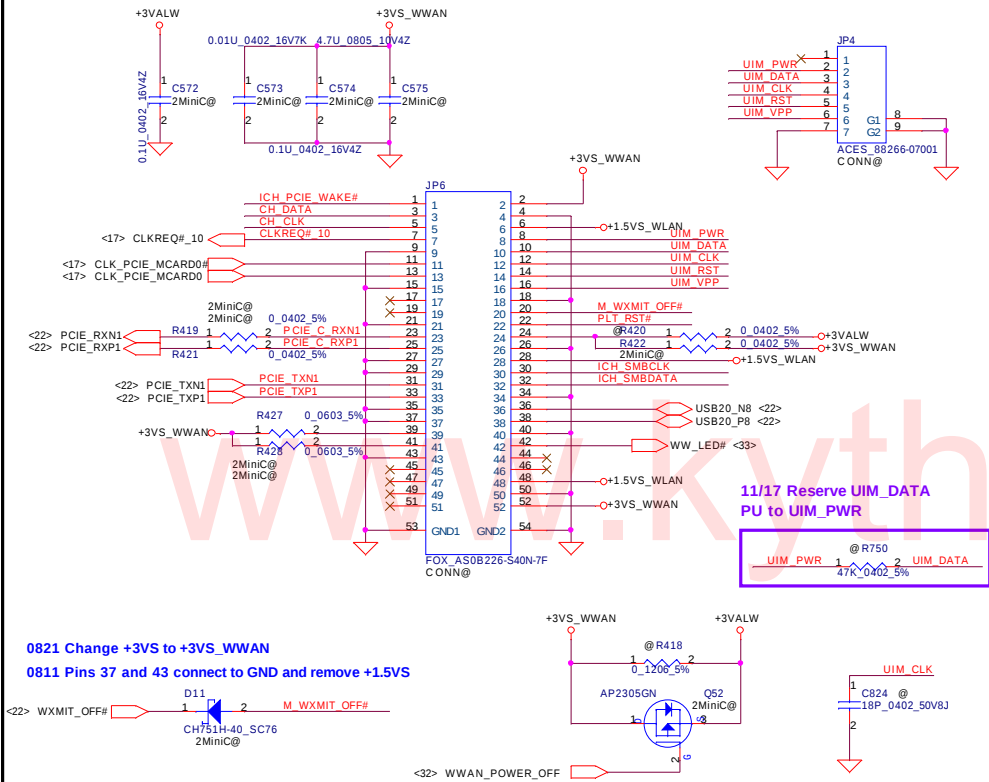
ZZZZ
PCB-MB

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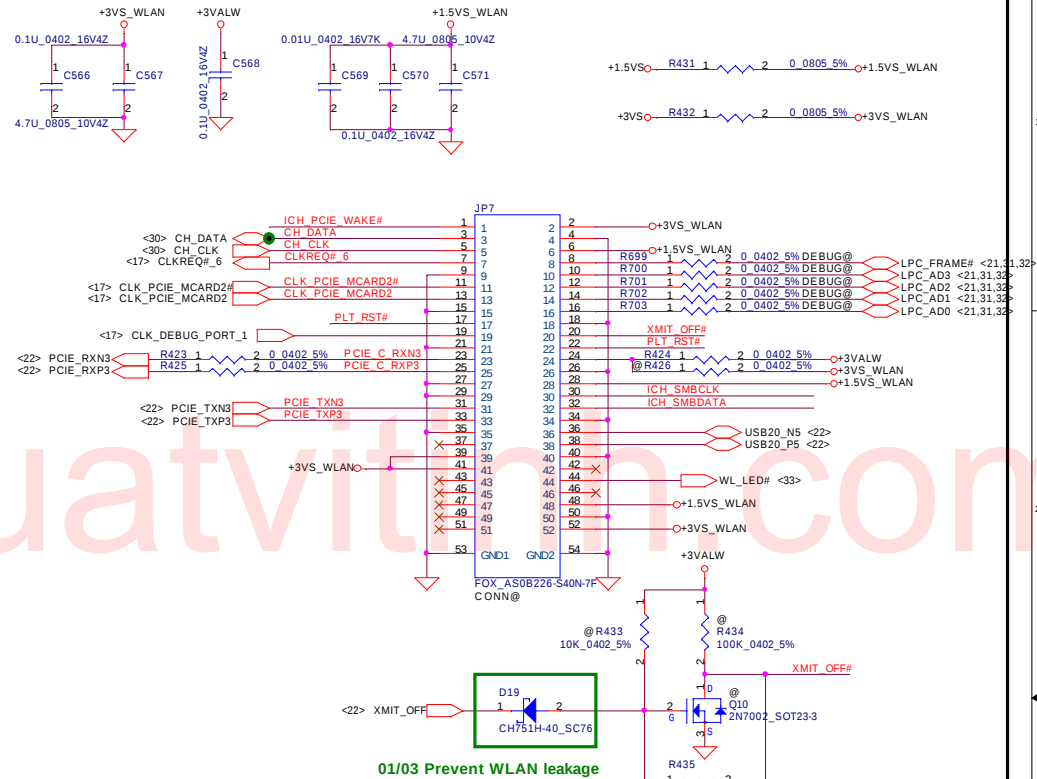


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				Cup	Montevia Blade UMA LA4101P	0.3
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Mini Card 0--TV tuner/WWAN/Robson SIM card Connector

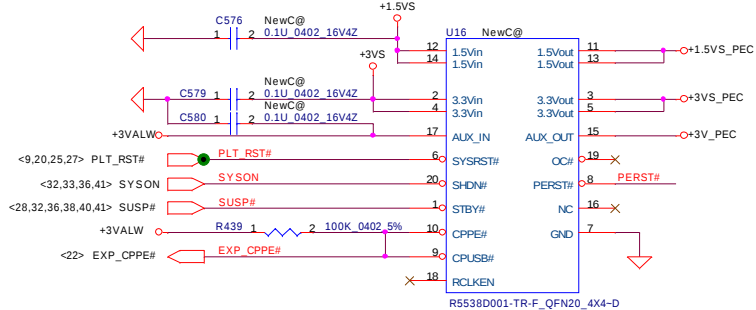


Mini Card 2---WLAN



New Card

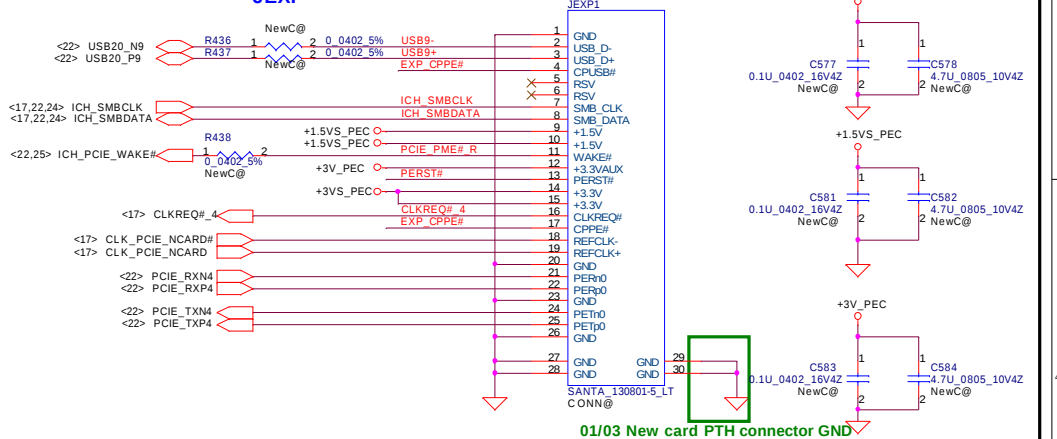
Express Card Power Switch



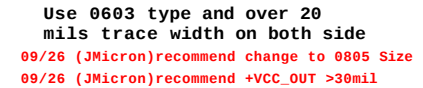
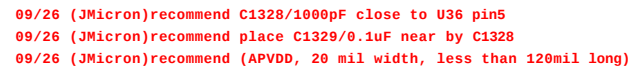
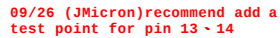
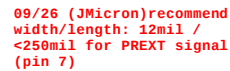
internal pull high to 3.3Vaux-in
EC need setting at Hi-Z & output Low

Close to
1EYD

Near to Express Card slot.



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Compal Electronics, Inc.

USB CardReader&CONN

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(4.75V(4.56~4.94V))
300mA



SENSE A		SENSE B	
Port	Resistor	Port	Resistor
A	39.2K	E	39.2K
B	20K	F	20K
C	10K	G	10K
D	5.11K	H	5.11K

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				Date:	Saturday, January 05, 2008	Sheet 28 of 46

Left side USB Connector

The diagram illustrates the left side USB connector circuit. It features a USB connector with pins 1 through 9. The circuit includes a USB connector symbol, a USB connector pinout table, and a circuit schematic. The schematic shows a USB connector with pins 1, 2, 3, 4, 5, 6, 7, 8, and 9. Pin 1 is connected to a 4.7uF capacitor (C1381) to ground. Pin 2 is connected to a 1500pF capacitor (C1380) to ground. Pin 3 is connected to a 0.1uF capacitor (C1382) to ground. Pin 4 is connected to a 10K resistor (R1083) to ground. Pin 5 is connected to a 1000pF capacitor (C1383) to ground. Pin 6 is connected to a 10K resistor (R1083) to ground. Pin 7 is connected to a 10K resistor (R1083) to ground. Pin 8 is connected to a 10K resistor (R1083) to ground. Pin 9 is connected to a 10K resistor (R1083) to ground. The USB connector is labeled 'USB_VCC' and 'USB_GND'. The circuit is labeled 'Left side USB Connector'.

USB Connector Pinout Table:

Pin	Signal
1	D-
2	D+
3	GND
4	VCC
5	NC
6	D-
7	D+
8	GND
9	VCC

Circuit Schematic:

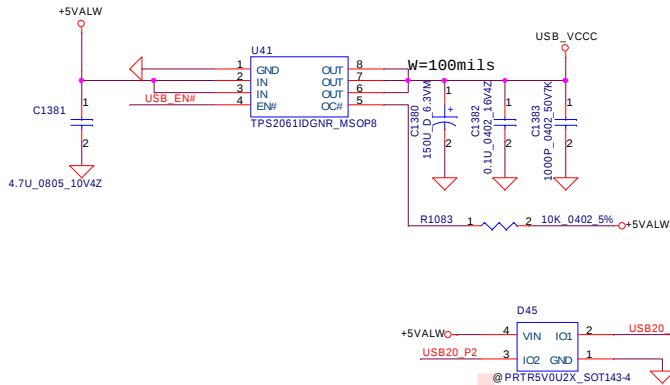
The circuit schematic shows a USB connector with pins 1, 2, 3, 4, 5, 6, 7, 8, and 9. Pin 1 is connected to a 4.7uF capacitor (C1381) to ground. Pin 2 is connected to a 1500pF capacitor (C1380) to ground. Pin 3 is connected to a 0.1uF capacitor (C1382) to ground. Pin 4 is connected to a 10K resistor (R1083) to ground. Pin 5 is connected to a 1000pF capacitor (C1383) to ground. Pin 6 is connected to a 10K resistor (R1083) to ground. Pin 7 is connected to a 10K resistor (R1083) to ground. Pin 8 is connected to a 10K resistor (R1083) to ground. Pin 9 is connected to a 10K resistor (R1083) to ground. The USB connector is labeled 'USB_VCC' and 'USB_GND'. The circuit is labeled 'Left side USB Connector'.

Component Values:

- C1381: 4.7uF
- C1380: 1500pF
- C1382: 0.1uF
- C1383: 1000pF
- R1083: 10K

USB Connector Label: USB_VCC, USB_GND

Component Reference: @PRT5V0U2X_SOT143-4



Left side ESATA/USB combination Connector

USB_VCCC

JP53

USB

VBUS 1

D- 2

D+ 3

GND 4

GND 5

A+ 6

A- 7

GND 8

GND 9

B+ 10

B- 11

GND 12

GND 13

GND 14

GND 15

TYCO_1759576-1 CONN@

<22> USB20_N2 R1080 1 2 0 0402 5% USB20_N2 R

<22> USB20_P2 R1081 1 2 0 0402 5% USB20_P2 R

<21> SATA_TXP5

<21> SATA_TXN5

<21> SATA_RXN5 C C1385 2 1 0.01U 0402 16V7K SATA_RXN5

<21> SATA_RXP5 C C1384 2 1 0.01U 0402 16V7K SATA_RXP5

ESATA@

SATA_TXP5

SATA_TXN5

SATA_RXN5

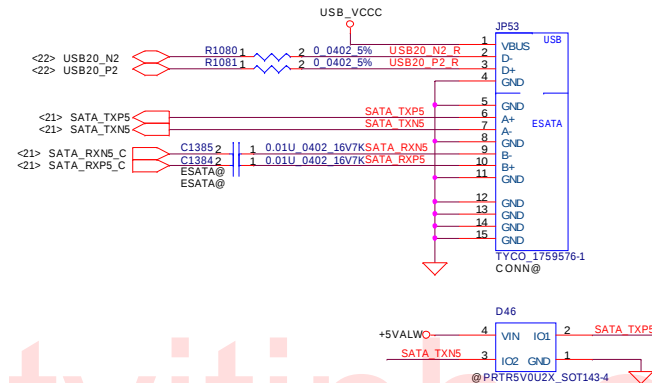
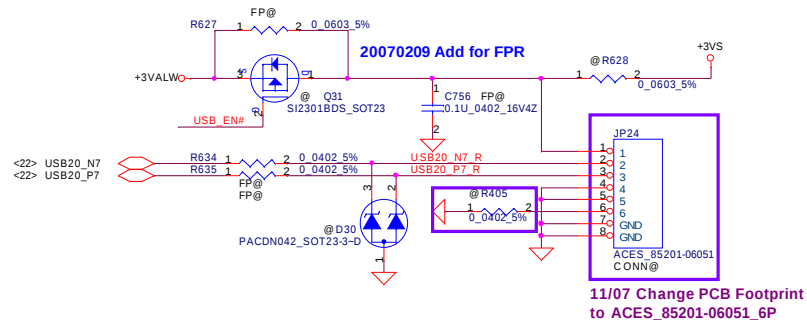
SATA_RXP5

D46

+5VALW0 4 VIN IO1 2 SATA_TXP5

SATA_TXN5 3 IO2 GND 1

@PRT85V0U2X_SOT143-4

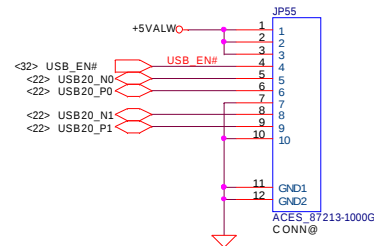
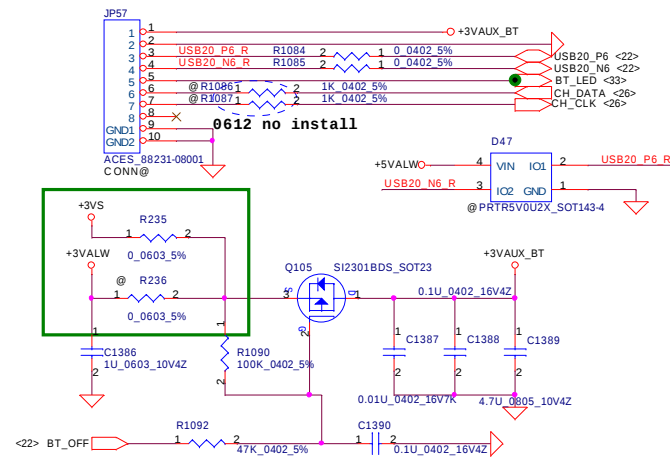
[illegible]

USB cable connector for Right side

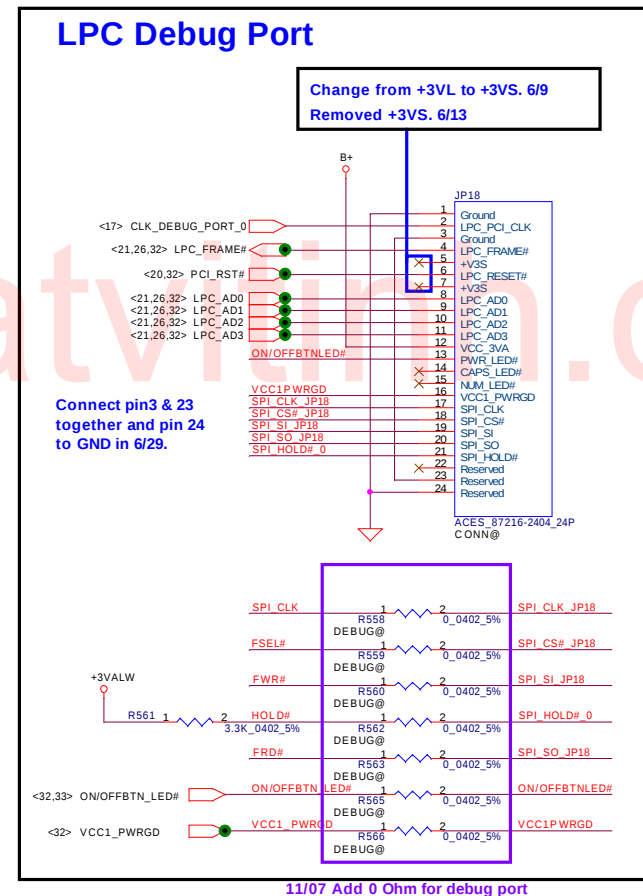
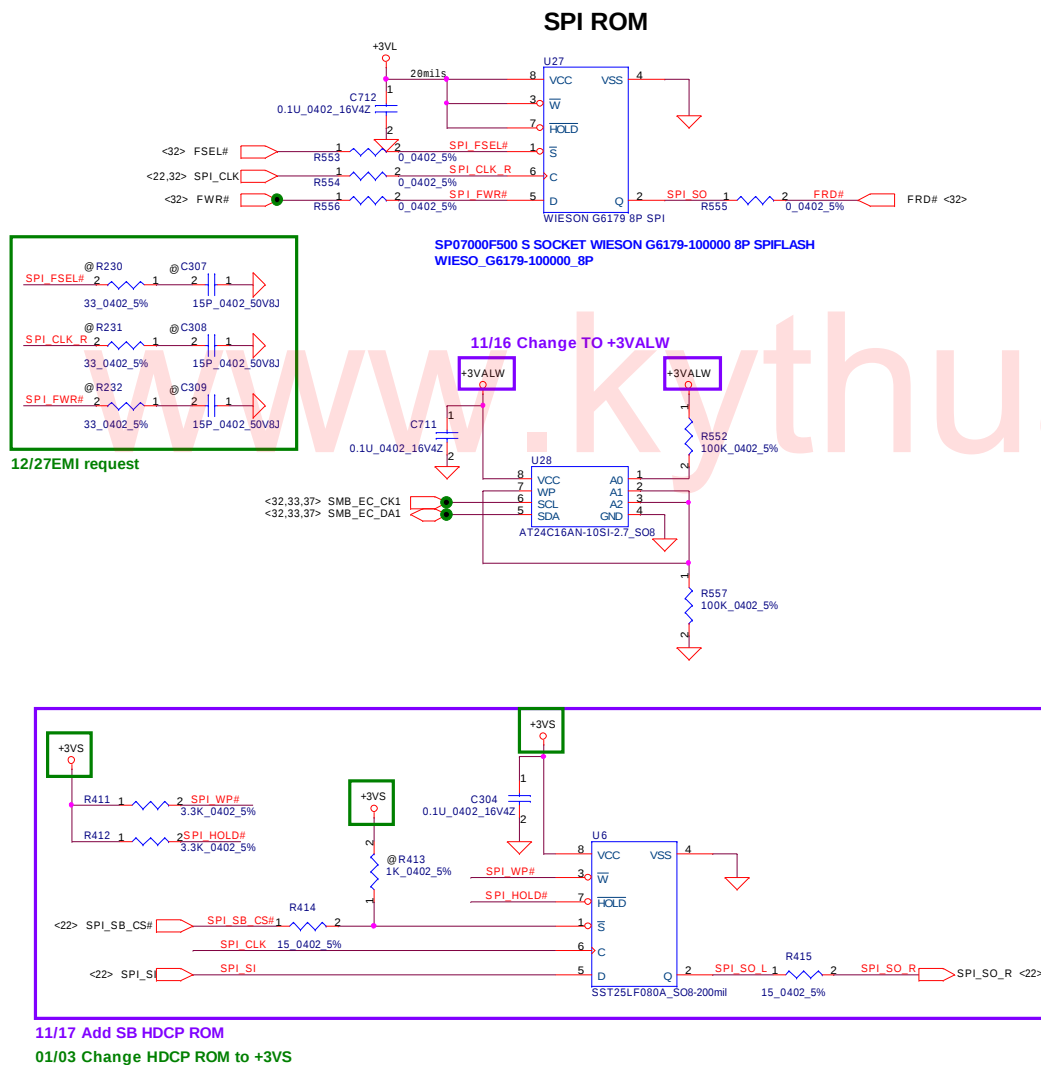
Wiring diagram for the USB cable connector for the Right side. The diagram shows a 12-pin connector (JPS5) with pins 1-10 connected to USB signals and pins 11-12 connected to ground. The signals are:

- +5VSWO (pin 1)
- USB_EN# (pin 2)
- USB20_N0 (pin 3)
- USB20_P0 (pin 4)
- USB20_N1 (pin 5)
- USB20_P1 (pin 6)
- GND1/GND2 (pins 11 and 12)

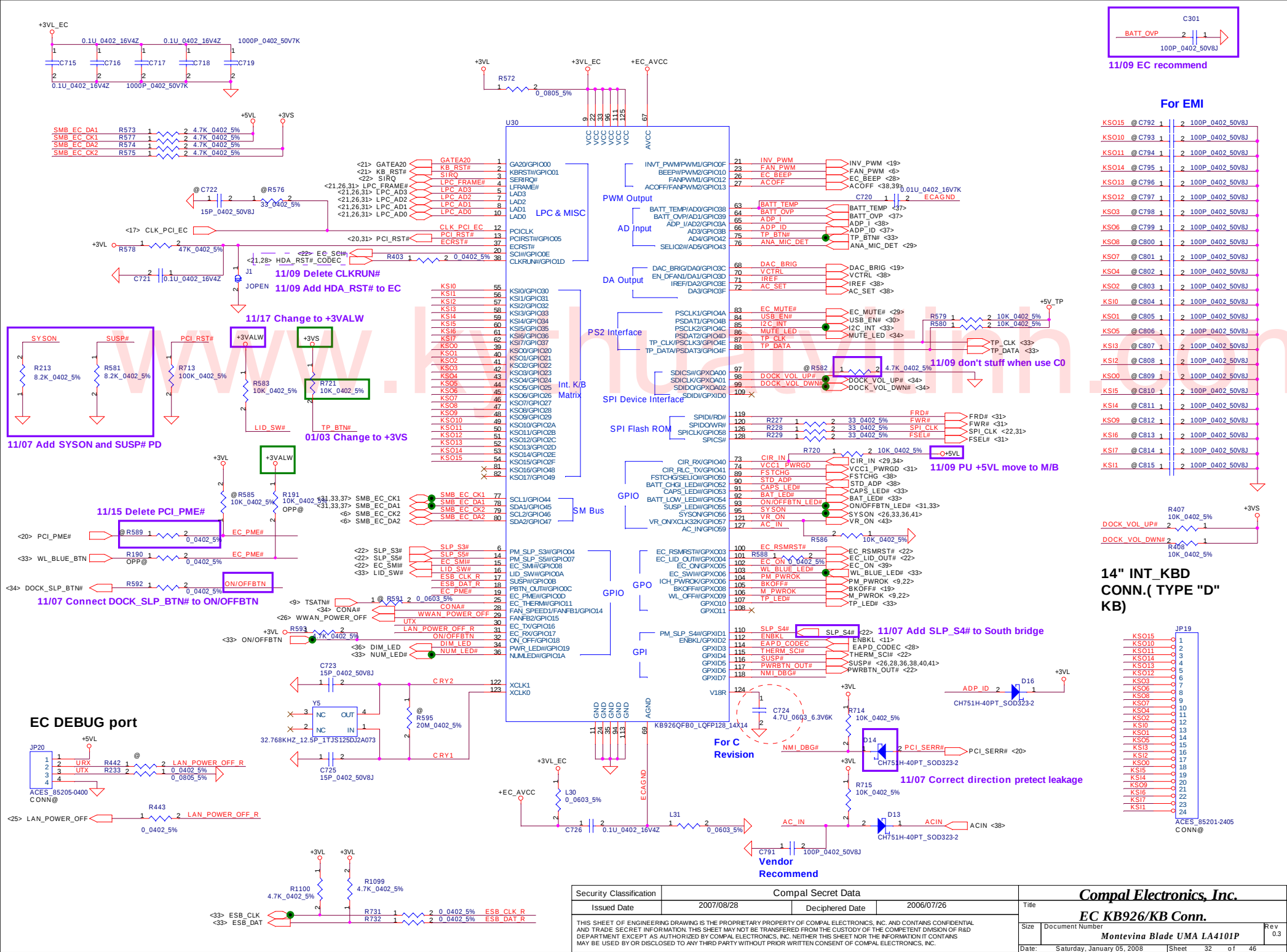
The connector is labeled JPS5 and the board is labeled ACES_87213-1000G CONN@.

[illegible]

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System LED

Cap lock

Battery Charge LED

HDD LED

System Power LED

Capacitor Sensor Conn

ON/OFF Button Connector

Diagram illustrating the wiring for the ON/OFF Button Connector (JP10).

Wiring connections:

- Pin 1: +5VALW_LED
- Pin 2: ON/OFFBTN
- Pin 3: ON/OFFBTN_LED#
- Pin 4: Ground
- Pin 5: G1
- Pin 6: G2

Connector Label: ACES 85201-04051 CONN@

Lid Switch Connector

Keyboard backlight Conn

Keyboard backlight Conn

The diagram shows a circuit for keyboard backlight connection. A green box labeled **R205** contains a resistor with a value of **0.0805 5%**. The resistor is connected to a **+5V_LED** pin. The other end of the resistor is connected to pin **1** of a **JP9** connector. Pin **2** of **JP9** is connected to pin **3** of a **G1/G2** connector. Pin **3** of **JP9** is connected to pin **4** of the **G1/G2** connector. Pin **4** of **JP9** is connected to pin **5** of the **G1/G2** connector. The **G1/G2** connector is labeled **ACES B5201-04051 CONN@**. The **G1/G2** connector has pins **5** and **6**, which are connected to a common ground symbol.

01/03 Keyboard backlight reserve a 0805 size resistor

Mini card LED

11/20 Reserve WW_LED function

T/P Board (Include T/P_ON/OFF)

TouchPAD ON/OFF LED

TP ON/OFF

SW1
TJG-533-V-TIR_6F

R611 @
10K_0402_5%

TP_BTN# <32>

11/07 Change part number

+5V_TP

+5V_LED

R610
820_0402_5%

R609
200_0402_5%

+5V

R718
10K_0402_5%

AMBER

White

D12
QSMF-C16E_AMBER-WHITE

TP_LED# <32>

2N7002_SOT23-3

On (TP_LED#=L)-> White
Off (TP_LED#=H)-> Amber

The schematic diagram illustrates the T/P Board Conn, featuring several key components and their interconnections:

- Power and Ground Connections:**
 - +5VALW** and **+5V_TP** are input power pins.
 - TP_CLK** and **TP_DATA** are data and clock pins.
 - EMI request** is a signal pin.
 - SYSON** is a signal pin, with a note indicating a range of $\langle 26, 32, 36, 41 \rangle$.
- Resistors:**
 - R691** and **R612** are resistors with values of $0.0603_5\%$ and $10K_0402_5\%$ respectively.
- Transistors:**
 - Q23** is a transistor with part number **SI2301BDS-T1-E3_SOT23-3**.
 - Q24** is a transistor with part number **2N7002_SOT23-3**.
- Capacitors:**
 - C729** is a capacitor with value $0.1\mu_0402_16V4Z$.
 - C730** and **C731** are capacitors with value $100P_0402_50VBJ$.
- Connectors and Packages:**
 - JP23** is a connector with pins 1, 2, 3, 4, 5, 6.
 - ACES_85201-04051** is a component with pins 1, 2, 3, 4, 5, 6.
 - D28** is a component with part number **PSOT24C_SOT23-3**.

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11/12 Change to +3V_L

Dock PRESENT

<32> CONA#

Dock PRESENT

R979
22_0402_5%
DOCK@

R623
2K_0402_5%
DOCK@

R621
10K_0402_5%

+3V_L

Q27
2N7002_SOT23-3
DOCK@

The schematic diagram illustrates the power and signal paths of the DOCK@ board. A blue box highlights the inductor L36 and L37, with a note "Need 600 Ohm 500 mA". The diagram includes components such as L36, L37, C754, C755, R625, R626, R632, R633, Q30, Q32, Q29, and various connectors. The board is powered by +3VS and has a GND connection. The signal paths for DOCK_MIC_R, DOCK_MIC_L, and DOCK_MIC_L_C are shown. The board also has a SENSE_B# connector and a DOCK@ connector.

O GND
 K LOUT_R
 K LOUT_L
 K MIC_R_C
 K MIC_L_C
 O IGND
 K PRESENT

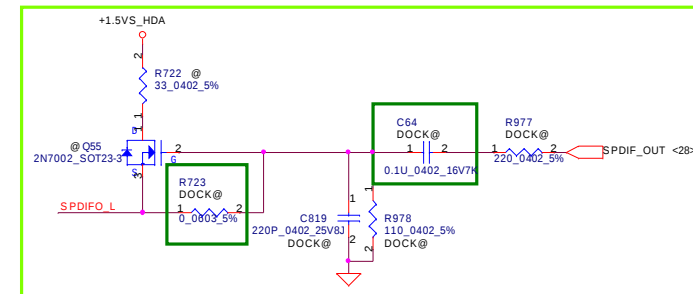
GND
 DOCKVIN

R620 1 2.2K 0.402 5%

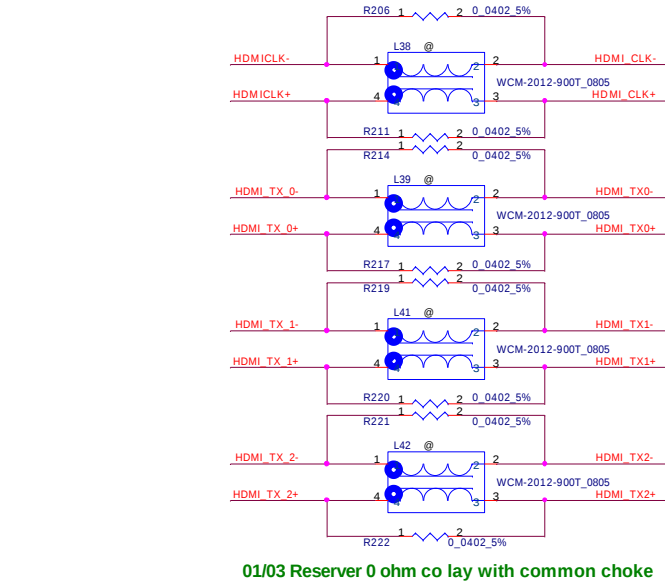
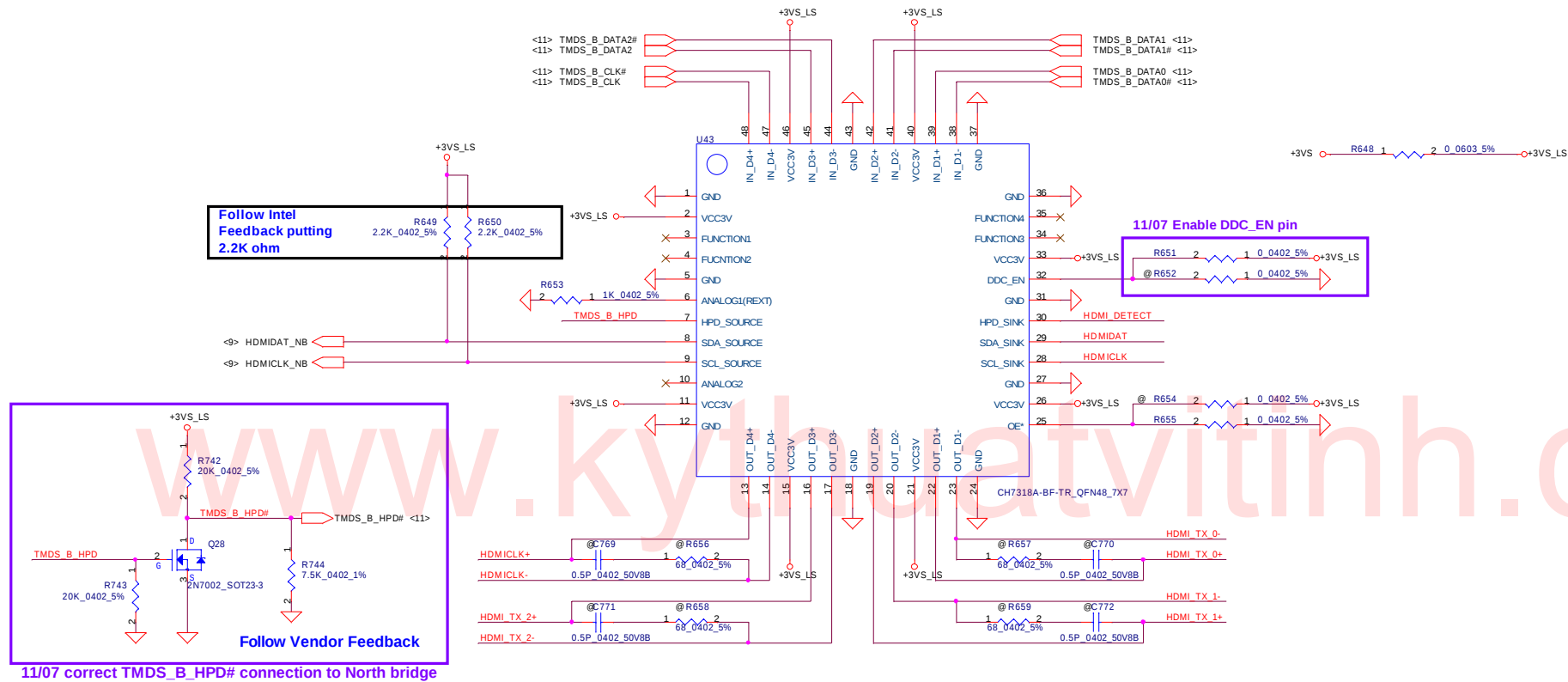
C305 1 2 1000P_0402 50V7K
 C306 1 2 1000P_0402 50V7K

GND

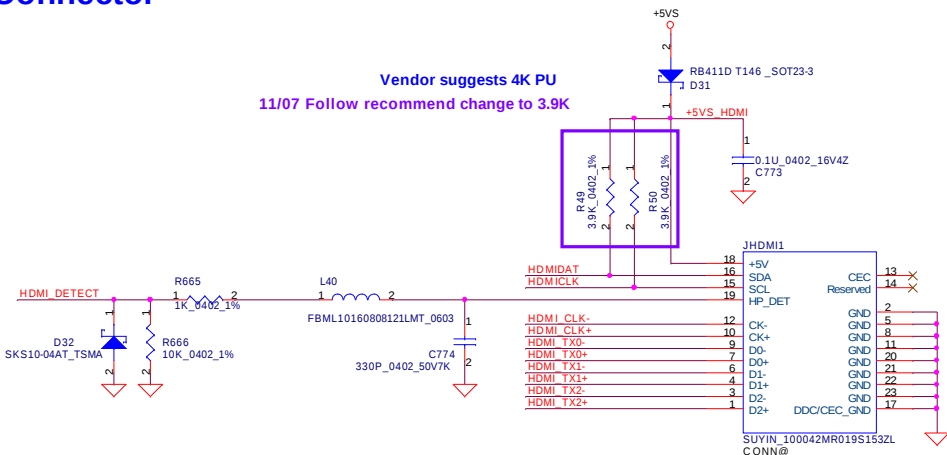
11/17 Recommend



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					Rev	
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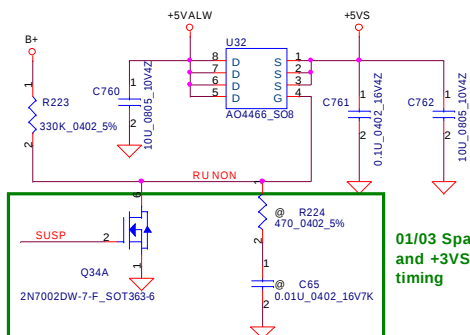


HDMI Connector

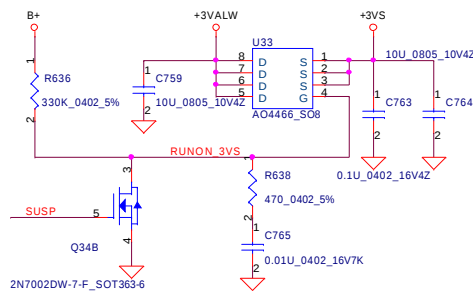


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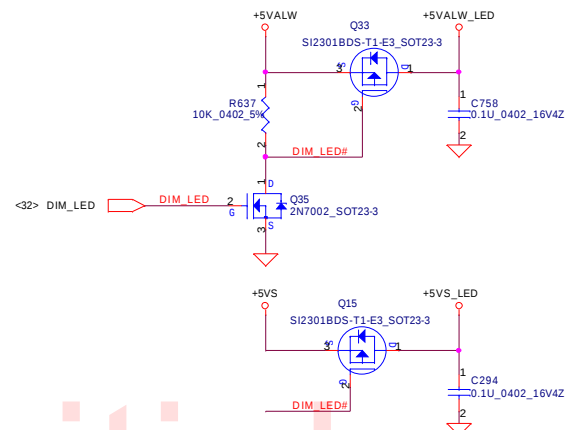
+5VALW to +5VS Transfer



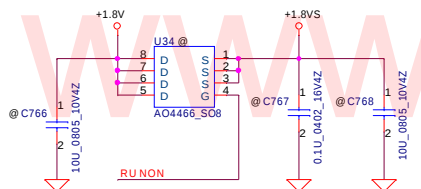
+3VALW to +3VS Transfer



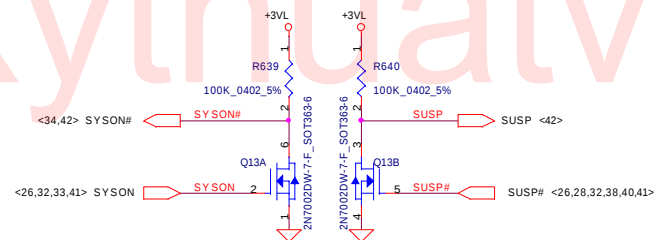
DIM LED



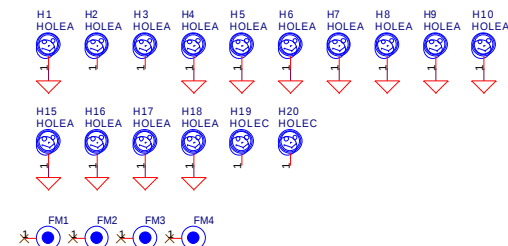
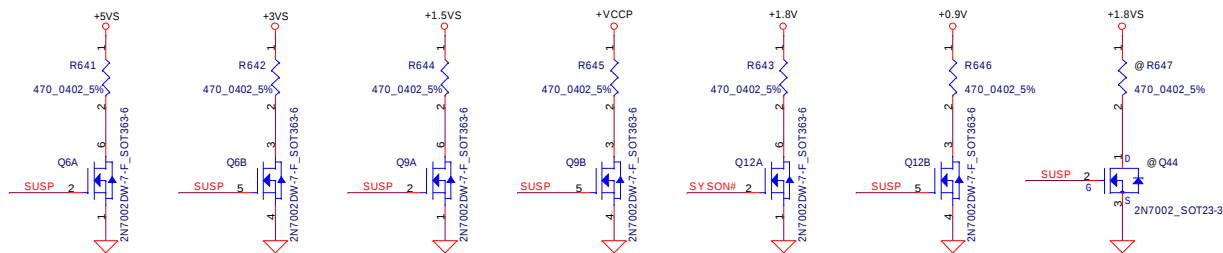
+1.8V to +1.8VS Transfer



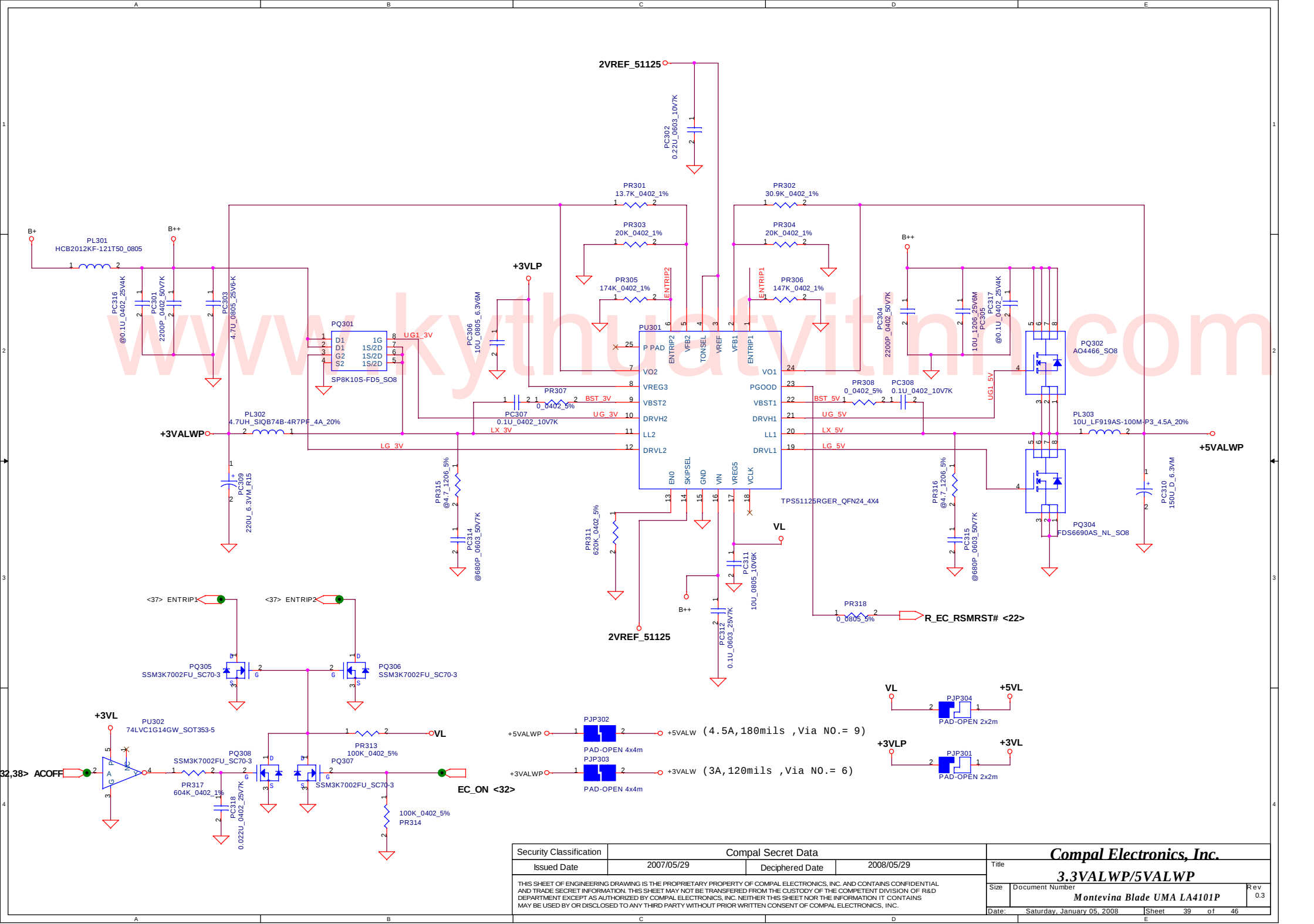
11/07 BOM Delete +1.8VS for Cardreader internal LDO

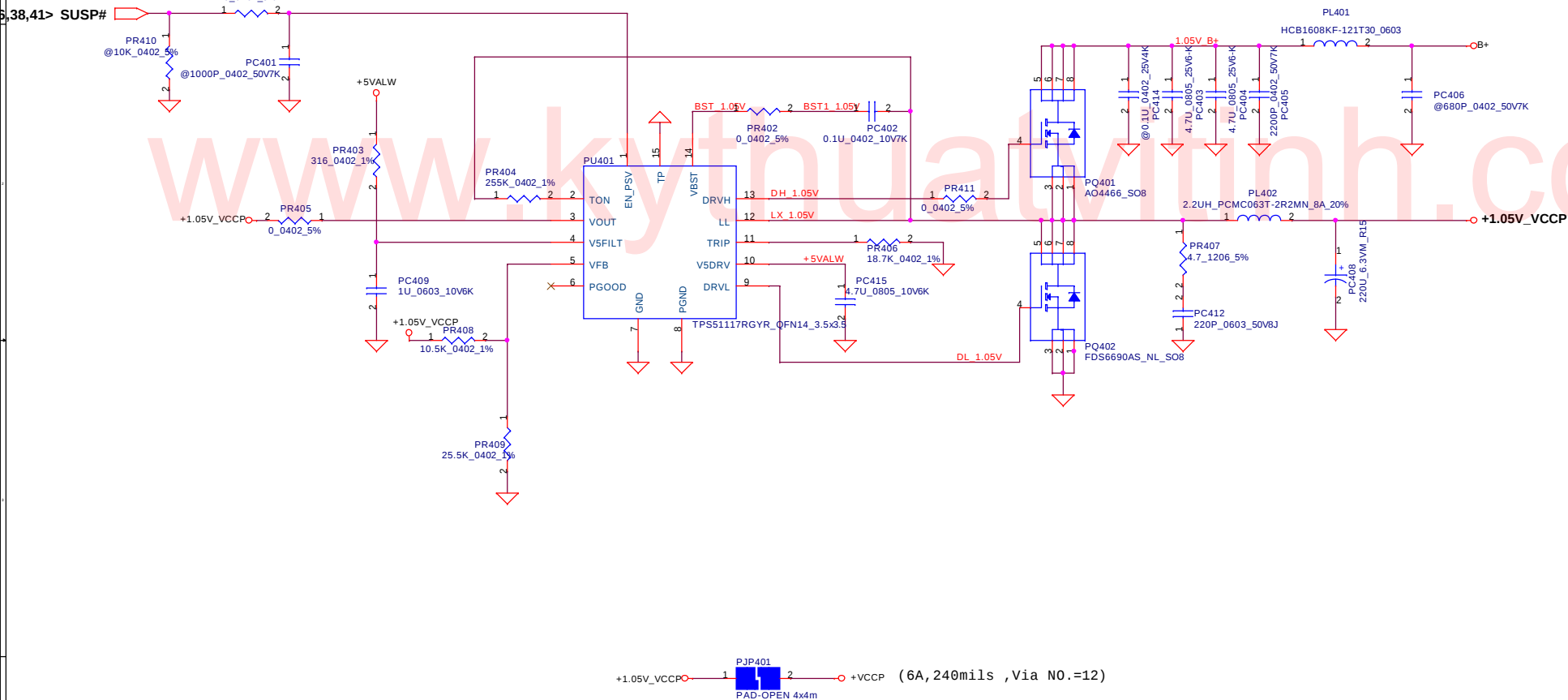


Discharge circuit

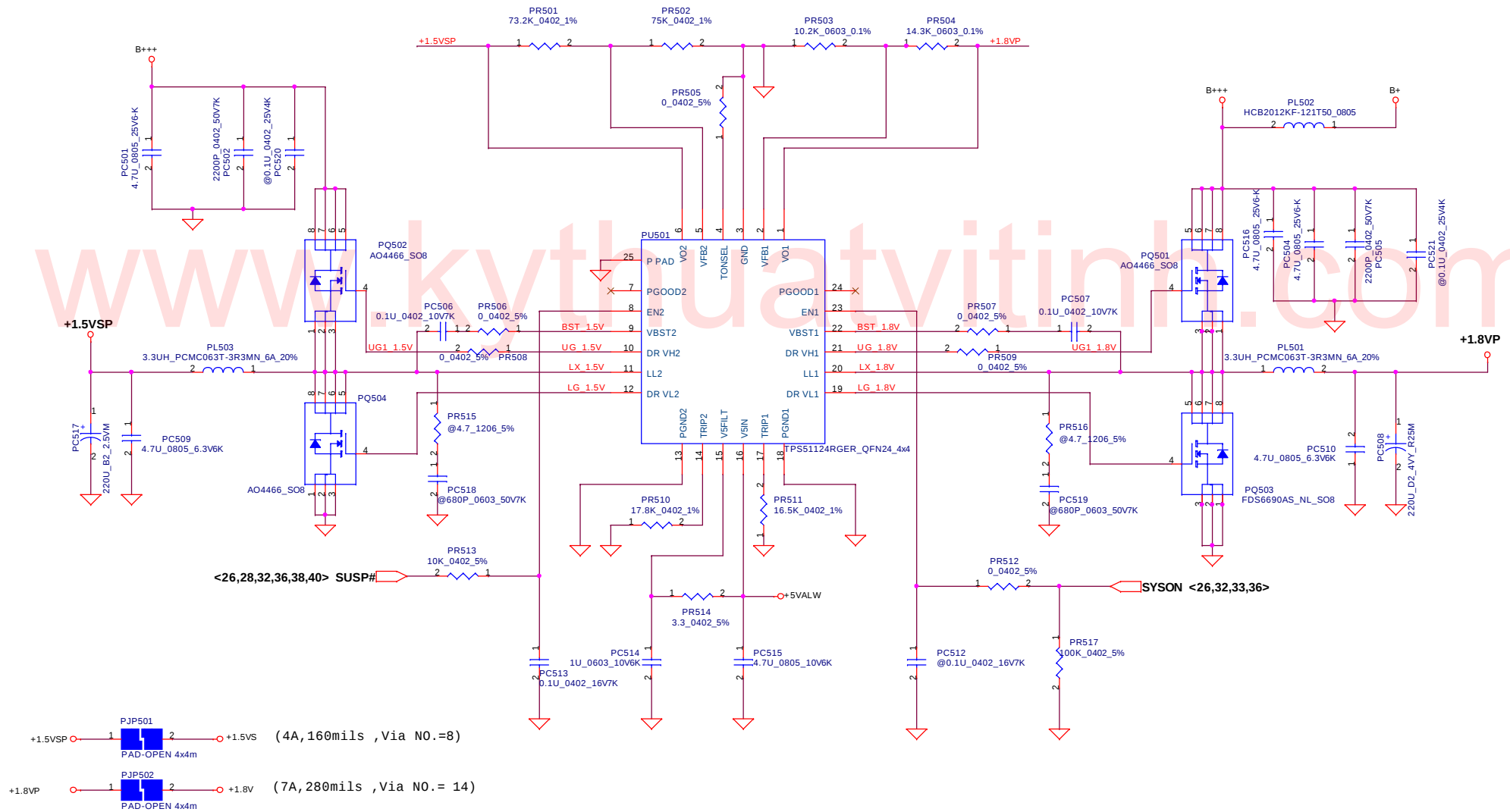


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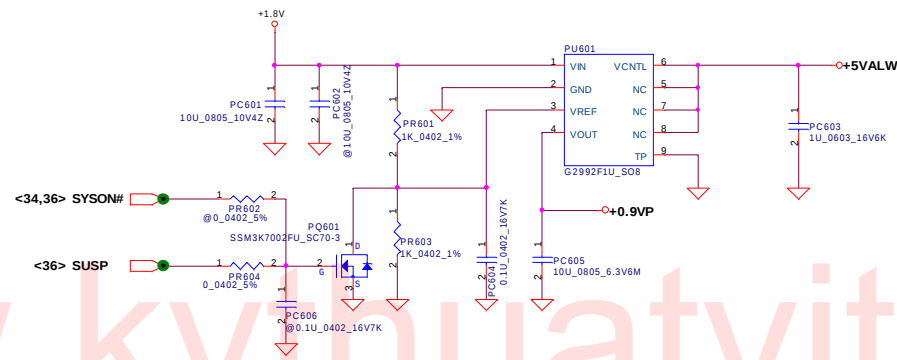




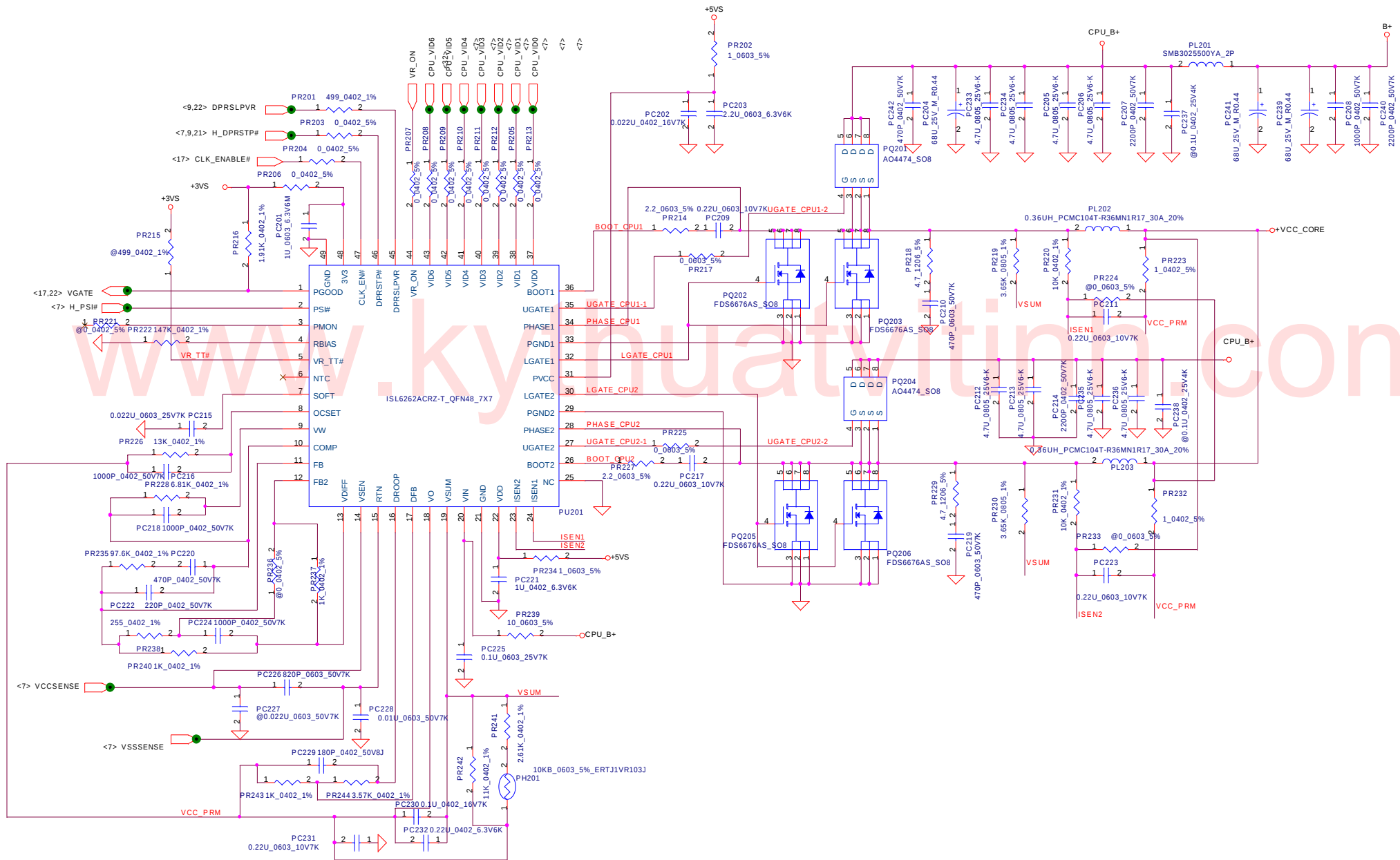
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Item	Fixed Issue (Reason for change)	PAGE	Modify List	Date	Phase
1	Transation Fail	08	C41、C42、C43、C44 Change ESR=7m ohm	11/21	SI-1
2	Disable TV out function from Docking	11、34	R61、R62、R63 change to 75 Ohm、TV_DCONSEL_0、TV_DCONSEL_1 connect to GND	11/07	SI-1
3	Update Connetor Library		CRT(JCRT1)、HDMI(JHDMI1)、ESATA(JP53)、Finger print(JJP24)、FAN(JP2)、Speaker(JP60)、Multi bay(JP12)、Dual LED(D53、D12)	11/17	SI-1
4	Delete LVDS B channel	11、19	Schematic Delete	11/17	SI-1
5	USB camera Footprint error	19	Change U42 to G916-390T1UF SOT23, it adjustable mode, R1091=215K、R1093=100K	11/07	SI-1
6	Reserve Card reader D3E function	22、27	GPIO6= CR_CPPE#、GPIO22=CR_WAKE#	11/17	SI-1
7	Swap PCIE LAN and New card	22	Swap PCIE4 and PICE6	11/17	SI-1
8	Add HDCP ROM for ICH9M	22、31	Add HDCP ROM for ICH9M	11/17	SI-1
9	Change G sensor control from SB、LED drive by +5VS	22、33	Change G sensor control from SB	11/17	SI-1
10	Avoid Battery mode can't boot issue	22、39	Add +3VALW GD to EC_RSMRST# to fix Battery mode can't boot issue	11/17	SI-1
11	Add G sensor ST and Bosch	24	Add G sensor ST and Bosch	11/17	SI-1
12	Change LAN solution (Marvell to Realtek)	25	Change LAN solution (Marvell to Realtek)	11/17	SI-1
13	LAN can't work	25	U46 Change to correct transformer type	11/17	SI-1
14	Cardreader schematic review and update, add D3E function	27	R709-->10K、R402-->8.2K、R704-->Stuff、R705-->@、U37-->@、Cardreader LED-->+5VS、add D3E function	11/17	SI-1
15	Jack can't detect normal	28	R1059 change from 39.2 to 39.2K	11/17	SI-1
16	Speaker work un normal	28	Add and Stuff C1362、R1065、R596	11/17	SI-1
17	HP audio team recommend	28、29	C285-C292、C1352、C1354 change to 0.022U、Amp output setup to 15.6 dB、Reserve C305、C306 for GNDA and GND	11/17	SI-1
18	Audio jack can't detect normal	29	Add Pull up resistor R401 to +3VALW	11/17	SI-1
19	Docking HP audio test fail	29	Add C295、C296 to avoid DC level, and add R409、R410 to reduce HP out level	11/17	SI-1
20	Leakage problem	32	Correct direction prectect leakage	11/07	SI-1
21	EC pin define update	32	Delete EC_PME#、SYSON PU、SUSP# PU、LID_SW# change to +3VALW、Delete CLKRUN#、R582->@ for C0 chip、CIR PU+5VL、add 100P to BATT_OVP(EC recommend)	11/07	SI-1
22	Can't Hibernation(SLP_S4#)	32	Connect SLP_S4# to SB	11/17	SI-1
23	EC can't receive docking present	34	CONA# change +3VL	11/12	SI-1
24	HDMI can't detect	35	DDC_EN must enable、TMDS_B_HPDP# inverse	11/07	SI-1
25	LVDS power on timing	19	C238 change to 0.047u to meet TI timing	01/03	SI-2
26	Prevent WWAN nosie	21	Add 12p on HDA_SDOUT and HDA_SDOUT	01/03	SI-2
27	Power leakage	21、31	Change HDCP ROM to +3VS power plane	01/03	SI-2
28	Prevent WLAN leakage	26	Add Diode prevent WLAN leakage	01/03	SI-2

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Item	Fixed Issue (Reason for change)	PAGE	Modify List	Date	Phase
29	New card PTH connector GND	26	New card PTH connector GND	01/03	SI-2
30	Change Cardreader LED control	27	Change Cardreader LED control	01/03	SI-2
31	Change SPDIF to SPDIF1	28	Change to SPDIF1	01/03	SI-2
32	Shut down pop noise	29	Change C293 to 1U	01/03	SI-2
33	Change BT power to +3VS	30	Change BT power to +3VS	01/03	SI-2
34	EMI Request	31	SPI_FSEL#、SPI_CLK_R、SPI_FWR# reserver RC	01/03	SI-2
35	Reserver 0 ohm co lay with common choke	35	Reserver 0 ohm co lay with common choke	01/03	SI-2
36	Sparate+5VS and +3VS power timing	36	Sparate+5VS and +3VS power timing	01/03	SI-2
37	Keyboard backlight reserve a 0805 size resistor	33	Keyboard backlight reserve a 0805 size resistor	01/03	SI-2
38	Change Lid switch connector type	33	Change Lid switch connector type	01/03	SI-2
39					
40					
41					
42					
43					
44					
45					
46					
47					
48					
49					
50					
51					
52					
53					
54					
55					
56					

Version Change List (P. I. R. List) for Power Circuit

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	1	37	DC Connector /CPU_OTP	11/06	Compal	Add PD4 & PC12	
	2	39	3.3VALWP/5VALWP	11/06	Compal	for Layout	
						Change PQ301 cancel PQ303	
	3	38	Charger	11/06	Compal	EMI solution	Add pc128
2	4	43	+CPU_CORE	11/06	Compal	EMI solution	Add PC240
	5	39	3.3VALWP/5VALWP	11/14	Compal	for Layout	Change PL303 and PC310
	6	38	Charger	12/31	Compal	EMI solution	Add PC129, PC130, PC131, PC132, PC133
	7	43	+CPU_CORE	12/31	Compal	EMI solution	Add PC242
	8	39	3.3VALWP/5VALWP	12/31	Compal	PWR request	Add PU302, control signal changed to AC0FF
3	9						
	10						
	11						
	12						
	13						
	14						
4							

Security Classification

Compal Secret Data

Issued Date

2007/08/02

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